

IOBP

NEPTUNE I/O BAY BACKPLANE

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REVISION HISTORY

REVISION	DATE	COMMENTS
0.0	03/06/91	INITIAL REVISION

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CONVEX DEFINE
X FIRST TO
Y STEP SIZE
DEF_MOUNT=DIP



CONVEX

TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0

ABBR: IOBP
ENGR: MICHAEL C. GREIM

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PAGE: 1

8

7

6

5

4

3

2

1

D

C

B

A

D

C

B

A

8

7

6

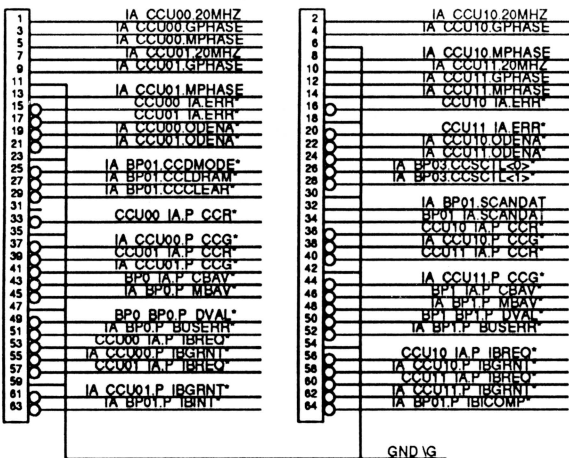
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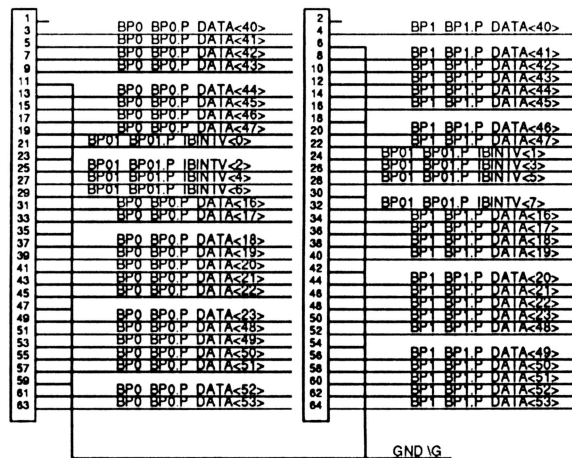
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PBUS 0&1 CONNECTOR

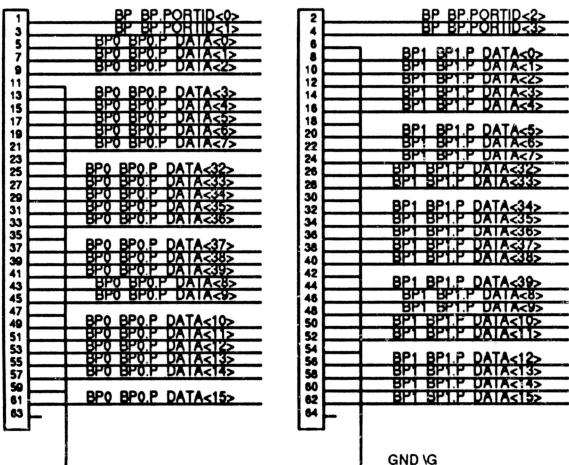
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MALE,PRESSFIT,STRAIGHT,EJECTORS
4P J15



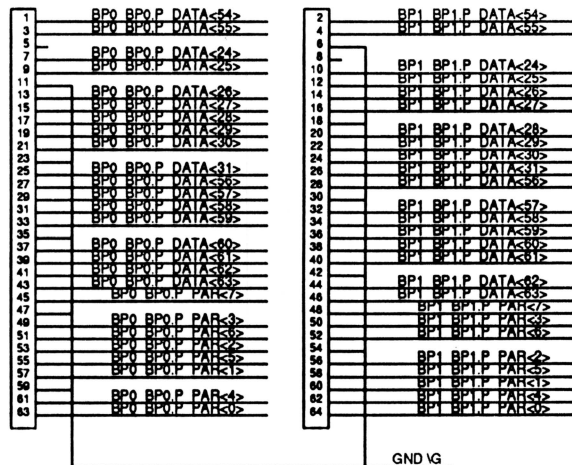
HDR64 301-000035-003
MALE,PRESSFIT,STRAIGHT,EJECTORS
1P J13



HDR64 301-000035-003
MALE,PRESSFIT,STRAIGHT,EJECTORS
3P J14



HDR64 301-000035-003
MALE,PRESSFIT,STRAIGHT,EJECTORS
2P J12



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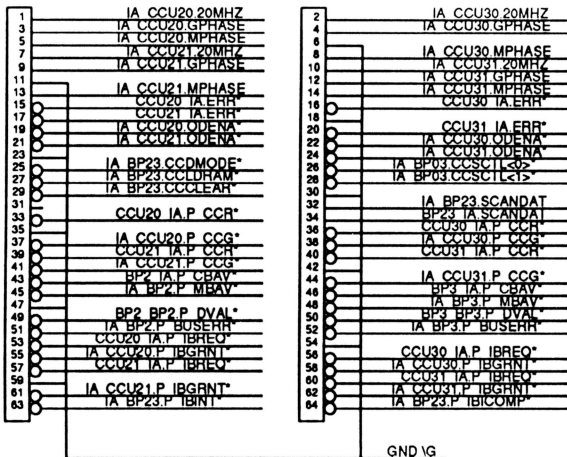
ABBR: IOBP
ENGR: NOBLES

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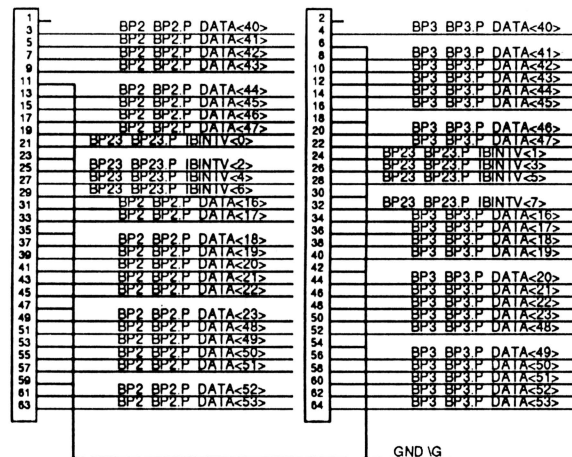
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PBUS 2&3 CONNECTOR

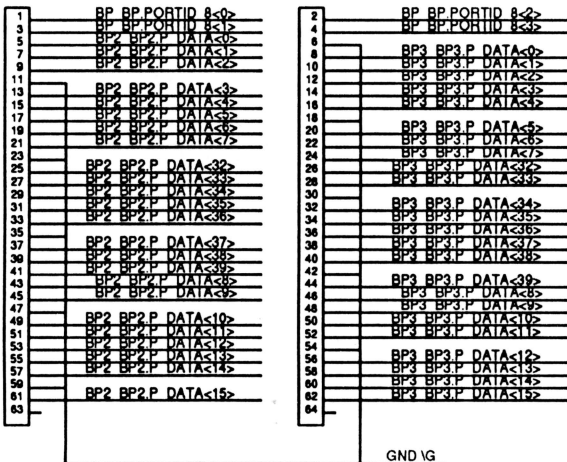
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MALE,PRESSFIT,STRAIGHT,EJECTORS
4P J19



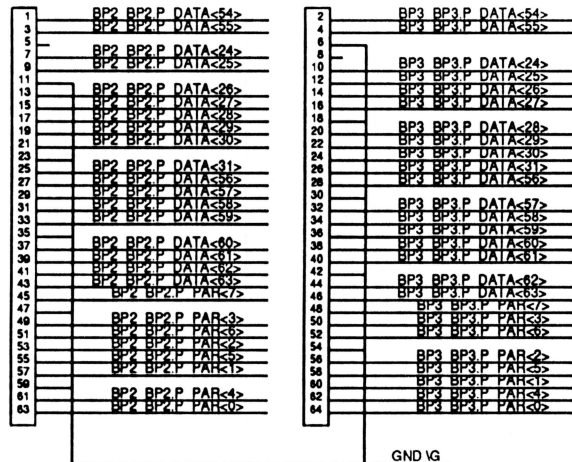
HDR64 301-000035-003
MALE,PRESSFIT,STRAIGHT,EJECTORS
1P J17



HDR64 301-000035-003
MALE,PRESSFIT,STRAIGHT,EJECTORS
3P J18



HDR64 301-000035-003
MALE,PRESSFIT,STRAIGHT,EJECTORS
2P J16



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TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0
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PAGE: 2

NIA P2 CONNECTOR

AUGAT-NGC-800
 BACKPLANE VERSION
 800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
 IA 1P
 BOARD=NIA

TOP
 J5
 J4
 J3
 J2
 J1
 BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND
 TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.).
 THE GED 'SECTION' COMMAND WILL NOT WORK.
 SEC=2

GROUND ALL PINS
 ON THIS ROW!

101	2 101	TTL	BP3 BP3 P DATA-36-IN20	201	2 201	TTL	BP2 BP2 P DATA-36-IN20	301	2 301	TTL	BP1 BP1 P DATA-36-IN20	401	2 401	TTL	BP0 BP0 P DATA-36-IN20	501	2 501	
102	2 102	TTL	BP3 BP3 P DATA-38-IN20	202	2 202	TTL	BP2 BP2 P DATA-38-IN20	302	2 302	TTL	BP1 BP1 P DATA-38-IN20	402	2 402	TTL	BP0 BP0 P DATA-38-IN20	502	2 502	
103	2 103	TTL	BP3 BP3 P PAR-3-IN20	203	2 203	TTL	BP2 BP2 P PAR-3-IN20	303	2 303	TTL	BP1 BP1 P PAR-3-IN20	403	2 403	TTL	BP0 BP0 P PAR-3-IN20	503	2 503	
104	2 104	TTL	BP3 BP3 P DATA-6-IN20	204	2 204	TTL	BP2 BP2 P DATA-6-IN20	304	2 304	TTL	BP1 BP1 P DATA-6-IN20	404	2 404	TTL	BP0 BP0 P DATA-6-IN20	504	2 504	
105	2 105	TTL	BP3 BP3 P DATA-8-IN20	205	2 205	TTL	BP2 BP2 P DATA-8-IN20	305	2 305	TTL	BP1 BP1 P DATA-8-IN20	405	2 405	TTL	BP0 BP0 P DATA-8-IN20	505	2 505	
106	2 106	TTL	BP3 BP3 P DATA-9-IN20	206	2 206	TTL	BP2 BP2 P DATA-9-IN20	306	2 306	TTL	BP1 BP1 P DATA-9-IN20	406	2 406	TTL	BP0 BP0 P DATA-9-IN20	506	2 506	
107	2 107	TTL	BP3 BP3 P DATA-11-IN20	207	2 207	TTL	BP2 BP2 P DATA-11-IN20	307	2 307	TTL	BP1 BP1 P DATA-11-IN20	407	2 407	TTL	BP0 BP0 P DATA-11-IN20	507	2 507	
108	2 108	TTL	BP3 BP3 P DATA-12-IN20	208	2 208	TTL	BP2 BP2 P DATA-12-IN20	308	2 308	TTL	BP1 BP1 P DATA-12-IN20	408	2 408	TTL	BP0 BP0 P DATA-12-IN20	508	2 508	
109	2 109	TTL	BP3 BP3 P DATA-13-IN20	209	2 209	TTL	BP2 BP2 P DATA-13-IN20	309	2 309	TTL	BP1 BP1 P DATA-13-IN20	409	2 409	TTL	BP0 BP0 P DATA-13-IN20	509	2 509	
110	2 110	TTL	BP3 BP3 P DATA-14-IN20	210	2 210	TTL	BP2 BP2 P DATA-14-IN20	310	2 310	TTL	BP1 BP1 P DATA-14-IN20	410	2 410	TTL	BP0 BP0 P DATA-14-IN20	510	2 510	
111	2 111	TTL	BP3 BP3 P DATA-15-IN20	211	2 211	TTL	BP2 BP2 P DATA-15-IN20	311	2 311	TTL	BP1 BP1 P DATA-15-IN20	411	2 411	TTL	BP0 BP0 P DATA-15-IN20	511	2 511	
112	2 112	TTL	BP3 BP3 P PAR-6-IN20	212	2 212	TTL	BP2 BP2 P PAR-6-IN20	312	2 312	TTL	BP1 BP1 P PAR-6-IN20	412	2 412	TTL	BP0 BP0 P PAR-6-IN20	512	2 512	
113	2 113	TTL	BP3 BP3 P DATA-40-IN20	213	2 213	TTL	BP2 BP2 P DATA-40-IN20	313	2 313	TTL	BP1 BP1 P DATA-40-IN20	413	2 413	TTL	BP0 BP0 P DATA-40-IN20	513	2 513	
114	2 114	TTL	BP3 BP3 P DATA-41-IN20	214	2 214	TTL	BP2 BP2 P DATA-41-IN20	314	2 314	TTL	BP1 BP1 P DATA-41-IN20	414	2 414	TTL	BP0 BP0 P DATA-41-IN20	514	2 514	
115	2 115	TTL	BP3 BP3 P DATA-42-IN20	215	2 215	TTL	BP2 BP2 P DATA-42-IN20	315	2 315	TTL	BP1 BP1 P DATA-42-IN20	415	2 415	TTL	BP0 BP0 P DATA-42-IN20	515	2 515	
116	2 116	TTL	BP3 BP3 P DATA-43-IN20	216	2 216	TTL	BP2 BP2 P DATA-43-IN20	316	2 316	TTL	BP1 BP1 P DATA-43-IN20	416	2 416	TTL	BP0 BP0 P DATA-43-IN20	516	2 516	
117	2 117	TTL	BP3 BP3 P DATA-44-IN20	217	2 217	TTL	BP2 BP2 P DATA-44-IN20	317	2 317	TTL	BP1 BP1 P DATA-44-IN20	417	2 417	TTL	BP0 BP0 P DATA-44-IN20	517	2 517	
118	2 118	TTL	BP3 BP3 P DATA-46-IN20	218	2 218	TTL	BP2 BP2 P DATA-46-IN20	318	2 318	TTL	BP1 BP1 P DATA-46-IN20	418	2 418	TTL	BP0 BP0 P DATA-46-IN20	518	2 518	
119	2 119	TTL	BP3 BP3 P DATA-48-IN20	219	2 219	TTL	BP2 BP2 P DATA-48-IN20	319	2 319	TTL	BP1 BP1 P DATA-48-IN20	419	2 419	TTL	BP0 BP0 P DATA-48-IN20	519	2 519	
120	2 120	TTL	BP3 BP3 P DATA-47-IN20	220	2 220	TTL	BP2 BP2 P DATA-47-IN20	320	2 320	TTL	BP1 BP1 P DATA-47-IN20	420	2 420	TTL	BP0 BP0 P DATA-47-IN20	520	2 520	
121	2 121	TTL	BP3 BP3 P PAR-2-IN20	221	2 221	TTL	BP2 BP2 P PAR-2-IN20	321	2 321	TTL	BP1 BP1 P PAR-2-IN20	421	2 421	TTL	BP0 BP0 P PAR-2-IN20	521	2 521	
122	2 122	TTL	BP23 BP23 P IBNTV-1-IN20	222	2 222	TTL	BP23 BP23 P IBNTV-1-IN20	322	2 322	TTL	BP21 BP21 P IBNTV-1-IN20	422	2 422	TTL	BP20 BP20 P IBNTV-1-IN20	522	2 522	
123	2 123	TTL	BP23 BP23 P IBNTV-3-IN20	223	2 223	TTL	BP23 BP23 P IBNTV-3-IN20	323	2 323	TTL	BP21 BP21 P IBNTV-3-IN20	423	2 423	TTL	BP20 BP20 P IBNTV-3-IN20	523	2 523	
124	2 124	TTL	BP23 BP23 P IBNTV-5-IN20	224	2 224	TTL	BP23 BP23 P IBNTV-5-IN20	324	2 324	TTL	BP21 BP21 P IBNTV-5-IN20	424	2 424	TTL	BP20 BP20 P IBNTV-5-IN20	524	2 524	
125	2 125	TTL	BP23 BP23 P IBNTV-7-IN20	225	2 225	TTL	BP23 BP23 P IBNTV-7-IN20	325	2 325	TTL	BP21 BP21 P IBNTV-7-IN20	425	2 425	TTL	BP20 BP20 P IBNTV-7-IN20	525	2 525	
126	2 126	TTL	BP3 BP3 P DATA-18-IN20	226	2 226	TTL	BP2 BP2 P DATA-18-IN20	326	2 326	TTL	BP1 BP1 P DATA-18-IN20	426	2 426	TTL	BP0 BP0 P DATA-18-IN20	526	2 526	
127	2 127	TTL	BP3 BP3 P DATA-17-IN20	227	2 227	TTL	BP2 BP2 P DATA-17-IN20	327	2 327	TTL	BP1 BP1 P DATA-17-IN20	427	2 427	TTL	BP0 BP0 P DATA-17-IN20	527	2 527	
128	2 128	TTL	BP3 BP3 P DATA-19-IN20	228	2 228	TTL	BP2 BP2 P DATA-19-IN20	328	2 328	TTL	BP1 BP1 P DATA-19-IN20	428	2 428	TTL	BP0 BP0 P DATA-19-IN20	528	2 528	
129	2 129	TTL	BP3 BP3 P DATA-19-IN20	229	2 229	TTL	BP2 BP2 P DATA-19-IN20	329	2 329	TTL	BP1 BP1 P DATA-19-IN20	429	2 429	TTL	BP0 BP0 P DATA-19-IN20	529	2 529	
130	2 130	TTL	BP3 BP3 P DATA-20-IN20	230	2 230	TTL	BP2 BP2 P DATA-20-IN20	330	2 330	TTL	BP1 BP1 P DATA-20-IN20	430	2 430	TTL	BP0 BP0 P DATA-20-IN20	530	2 530	
131	2 131	TTL	BP3 BP3 P DATA-21-IN20	231	2 231	TTL	BP2 BP2 P DATA-21-IN20	331	2 331	TTL	BP1 BP1 P DATA-21-IN20	431	2 431	TTL	BP0 BP0 P DATA-21-IN20	531	2 531	
132	2 132	EDL	NXP DATA-64-IN20	232	2 232	EDL	NXP DATA-24-IN20	332	2 332	EDL	NXP DATA-48-IN20	432	2 432	EDL	NXP DATA-16-IN20	532	2 532	
133	2 133	EDL	NXP DATA-62-IN20	233	2 233	EDL	NXP DATA-22-IN20	333	2 333	EDL	NXP DATA-46-IN20	433	2 433	EDL	NXP DATA-14-IN20	533	2 533	
134	2 134	EDL	NXP DATA-60-IN20	234	2 234	EDL	NXP DATA-20-IN20	334	2 334	EDL	NXP DATA-44-IN20	434	2 434	EDL	NXP DATA-12-IN20	534	2 534	
135	2 135	EDL	NXP DATA-58-IN20	235	2 235	EDL	NXP DATA-18-IN20	335	2 335	EDL	NXP DATA-42-IN20	435	2 435	EDL	NXP DATA-10-IN20	535	2 535	
136	2 136	EDL	NXP DATA-56-IN20	236	2 236	EDL	NXP DATA-16-IN20	336	2 336	EDL	NXP DATA-40-IN20	436	2 436	EDL	NXP DATA-8-IN20	536	2 536	
137	2 137	EDL	NXP DATA-54-IN20	237	2 237	EDL	NXP DATA-14-IN20	337	2 337	EDL	NXP DATA-38-IN20	437	2 437	EDL	NXP DATA-6-IN20	537	2 537	
138	2 138	EDL	NXP DATA-52-IN20	238	2 238	EDL	NXP DATA-12-IN20	338	2 338	EDL	NXP DATA-36-IN20	438	2 438	EDL	NXP DATA-4-IN20	538	2 538	
139	2 139	EDL	NXP DATA-50-IN20	239	2 239	EDL	NXP DATA-10-IN20	339	2 339	EDL	NXP DATA-34-IN20	439	2 439	EDL	NXP DATA-2-IN20	539	2 539	
140	2 140	EDL	NXP PAR-0-IN20	240	2 240	EDL	NXP PAR-4-IN20	340	2 340	EDL	NXP PAR-12-IN20	440	2 440	EDL	NXP PAR-8-IN20	540	2 540	GND VG

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TITLE: IOBP
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ABBR: IOBP
 ENGR: NOBLES

PAGE: 5

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2

1

NIA P4 CONNECTOR

AUGAT-NGC-800
BACKPLANE VERSION
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
IA 1P

BOARD=NIA

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.

SEC=4

MEMORY_DATA<47..40>
MEMORY_DATA<83..58>

MEMORY_DATA<15..8>
MEMORY_DATA<31..24>

MEMORY_DATA<39..32>
MEMORY_DATA<55..48>

MEMORY_DATA<7..0>
MEMORY_DATA<23..16>

GROUND ALL PINS
ON THIS ROW

101	4	101	ECL	XSO IA A REQ NEXTWZ	201	4	201	ECL	XSE IA A REQ NEXTWZ	301	4	301	ECL	IA XG SOFT EFFORWZ	401	4	401	ECL	XC IA B LOG ENAWZ	501	4	501	ECL	GND VG
102	4	102	ECL	XSO IA B REQ NEXTWZ	202	4	202	ECL	XSE IA B REQ NEXTWZ	302	4	302	ECL	XC IA RESETWZ	402	4	402	ECL	XC IA CCU CLEARWZ	502	4	502	ECL	
103	4	103	ECL	XSO IA A BIT PENDWZ	203	4	203	ECL	XSE IA A BIT PENDWZ	303	4	303	ECL	XC IA CLOCK SYNCWZ	403	4	403	ECL	XC IA CPU CNTR ENAWZ	503	4	503	ECL	
104	4	104	ECL	XSO IA B BIT PENDWZ	204	4	204	ECL	XSE IA B BIT PENDWZ	304	4	304	ECL	XC IA TRAP VEC1<7>WZ	404	4	404	ECL	XC IA CCU RBE<7>WZ	504	4	504	ECL	
105	4	105	ECL	XSO IA A REQ PEROWZ	205	4	205	ECL	XSE IA A REQ PEROWZ	305	4	305	ECL	XC IA TRAP VEC1<8>WZ	405	4	405	ECL	XC IA CCU RBE<8>WZ	505	4	505	ECL	
106	4	106	ECL	XSO IA B REQ PEROWZ	206	4	206	ECL	XSE IA B REQ PEROWZ	306	4	306	ECL	XC IA TRAP VEC1<9>WZ	406	4	406	ECL	XC IA CCU RBE<9>WZ	506	4	506	ECL	
107	4	107	ECL	IA XSO RDWZ	207	4	207	ECL	IA XSE RDWZ	307	4	307	ECL	XC IA TRAP VEC1<10>WZ	407	4	407	ECL	XC IA CCU RBE<10>WZ	507	4	507	ECL	
108	4	108	ECL	XC IA CCU STATUS FNNWZ	208	4	208	ECL	XC IA TRAP TYPE<3>WZ	308	4	308	ECL	XC IA TRAP VEC1<11>WZ	408	4	408	ECL	XC IA CCU RBE<11>WZ	508	4	508	ECL	
109	4	109	ECL	XC IA CPU BIT15WZ	209	4	209	ECL	XC IA TRAP TYPE<2>WZ	309	4	309	ECL	XC IA TRAP VEC1<12>WZ	409	4	409	ECL	XC IA CCU RBE<12>WZ	509	4	509	ECL	
110	4	110	ECL	IA XG TRAP 008PWZ	210	4	210	ECL	XC IA TRAP TYPE<11>WZ	310	4	310	ECL	XC IA TRAP VEC1<13>WZ	410	4	410	ECL	XC IA CCU RBE<13>WZ	510	4	510	ECL	
111	4	111	ECL	XC IA TRAP 009WZ	211	4	211	ECL	XC IA TRAP TYPE<10>WZ	311	4	311	ECL	XC IA TRAP VEC1<14>WZ	411	4	411	ECL	XC IA CCU RBE<14>WZ	511	4	511	ECL	
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114	4	114	ECL	XPE IARD DATA<14>WZ	214	4	214	ECL	XPO IARD DATA<14>WZ	314	4	314	ECL	XPE IARD DATA<8>WZ	414	4	414	ECL	XPO IARD DATA<8>WZ	514	4	514	ECL	
115	4	115	ECL	XPE IARD DATA<13>WZ	215	4	215	ECL	XPO IARD DATA<13>WZ	315	4	315	ECL	XPE IARD DATA<9>WZ	415	4	415	ECL	XPO IARD DATA<9>WZ	515	4	515	ECL	
116	4	116	ECL	XPE IARD DATA<12>WZ	216	4	216	ECL	XPO IARD DATA<12>WZ	316	4	316	ECL	XPE IARD DATA<10>WZ	416	4	416	ECL	XPO IARD DATA<10>WZ	516	4	516	ECL	
117	4	117	ECL	XPE IARD DATA<11>WZ	217	4	217	ECL	XPO IARD DATA<11>WZ	317	4	317	ECL	XPE IARD DATA<11>WZ	417	4	417	ECL	XPO IARD DATA<11>WZ	517	4	517	ECL	
118	4	118	ECL	XPE IARD DATA<10>WZ	218	4	218	ECL	XPO IARD DATA<10>WZ	318	4	318	ECL	XPE IARD DATA<12>WZ	418	4	418	ECL	XPO IARD DATA<12>WZ	518	4	518	ECL	
119	4	119	ECL	XPE IARD DATA<9>WZ	219	4	219	ECL	XPO IARD DATA<9>WZ	319	4	319	ECL	XPE IARD DATA<13>WZ	419	4	419	ECL	XPO IARD DATA<13>WZ	519	4	519	ECL	
120	4	120	ECL	XPE IARD DATA<8>WZ	220	4	220	ECL	XPO IARD DATA<8>WZ	320	4	320	ECL	XPE IARD DATA<14>WZ	420	4	420	ECL	XPO IARD DATA<14>WZ	520	4	520	ECL	
121	4	121	ECL	XPE IARD PAR<9>WZ	221	4	221	ECL	XPO IARD PAR<9>WZ	321	4	321	ECL	XPE IARD PAR<11>WZ	421	4	421	ECL	XPO IARD PAR<11>WZ	521	4	521	ECL	
122	4	122	ECL	XPE IARD DATA<31>WZ	222	4	222	ECL	XPO IARD DATA<31>WZ	322	4	322	ECL	XPE IARD DATA<23>WZ	422	4	422	ECL	XPO IARD DATA<23>WZ	522	4	522	ECL	
123	4	123	ECL	XPE IARD DATA<30>WZ	223	4	223	ECL	XPO IARD DATA<30>WZ	323	4	323	ECL	XPE IARD DATA<24>WZ	423	4	423	ECL	XPO IARD DATA<24>WZ	523	4	523	ECL	
124	4	124	ECL	XPE IARD DATA<29>WZ	224	4	224	ECL	XPO IARD DATA<29>WZ	324	4	324	ECL	XPE IARD DATA<25>WZ	424	4	424	ECL	XPO IARD DATA<25>WZ	524	4	524	ECL	
125	4	125	ECL	XPE IARD DATA<28>WZ	225	4	225	ECL	XPO IARD DATA<28>WZ	325	4	325	ECL	XPE IARD DATA<26>WZ	425	4	425	ECL	XPO IARD DATA<26>WZ	525	4	525	ECL	
126	4	126	ECL	XPE IARD DATA<27>WZ	226	4	226	ECL	XPO IARD DATA<27>WZ	326	4	326	ECL	XPE IARD DATA<27>WZ	426	4	426	ECL	XPO IARD DATA<27>WZ	526	4	526	ECL	
127	4	127	ECL	XPE IARD DATA<26>WZ	227	4	227	ECL	XPO IARD DATA<26>WZ	327	4	327	ECL	XPE IARD DATA<28>WZ	427	4	427	ECL	XPO IARD DATA<28>WZ	527	4	527	ECL	
128	4	128	ECL	XPE IARD DATA<25>WZ	228	4	228	ECL	XPO IARD DATA<25>WZ	328	4	328	ECL	XPE IARD DATA<29>WZ	428	4	428	ECL	XPO IARD DATA<29>WZ	528	4	528	ECL	
129	4	129	ECL	XPE IARD DATA<24>WZ	229	4	229	ECL	XPO IARD DATA<24>WZ	329	4	329	ECL	XPE IARD DATA<30>WZ	429	4	429	ECL	XPO IARD DATA<30>WZ	529	4	529	ECL	
130	4	130	ECL	XPO IARD RDWZ	230	4	230	ECL	XPE IARD RDWZ	330	4	330	ECL	IA XG RTRN PAR EFFWZ	430	4	430	ECL	IA XPE RTRN PAR EFFWZ	530	4	530	ECL	
131	4	131	ECL	NXP DATA<46>WZ	231	4	231	ECL	NXP DATA<48>WZ	331	4	331	ECL	NXP DATA<32>WZ	431	4	431	ECL	NXP DATA<40>WZ	531	4	531	ECL	
132	4	132	ECL	NXP DATA<41>WZ	232	4	232	ECL	NXP DATA<43>WZ	332	4	332	ECL	NXP DATA<33>WZ	432	4	432	ECL	NXP DATA<41>WZ	532	4	532	ECL	
133	4	133	ECL	NXP DATA<32>WZ	233	4	233	ECL	NXP DATA<34>WZ	333	4	333	ECL	NXP DATA<34>WZ	433	4	433	ECL	NXP DATA<42>WZ	533	4	533	ECL	
134	4	134	ECL	NXP DATA<53>WZ	234	4	234	ECL	NXP DATA<51>WZ	334	4	334	ECL	NXP DATA<35>WZ	434	4	434	ECL	NXP DATA<43>WZ	534	4	534	ECL	
135	4	135	ECL	NXP DATA<44>WZ	235	4	235	ECL	NXP DATA<45>WZ	335	4	335	ECL	NXP DATA<36>WZ	435	4	435	ECL	NXP DATA<44>WZ	535	4	535	ECL	
136	4	136	ECL	NXP DATA<45>WZ	236	4	236	ECL	NXP DATA<46>WZ	336	4	336	ECL	NXP DATA<37>WZ	436	4	436	ECL	NXP DATA<45>WZ	536	4	536	ECL	
137	4	137	ECL	NXP DATA<48>WZ	237	4	237	ECL	NXP DATA<49>WZ	337	4	337	ECL	NXP DATA<38>WZ	437	4	437	ECL	NXP DATA<46>WZ	537	4	537	ECL	
138	4	138	ECL	NXP DATA<47>WZ	238	4	238	ECL	NXP DATA<48>WZ	338	4	338	ECL	NXP DATA<39>WZ	438	4	438	ECL	NXP DATA<47>WZ	538	4	538	ECL	
139	4	139	ECL	NXP PAR<2>WZ	239	4	239	ECL	NXP PAR<4>WZ	339	4	339	ECL	NXP PAR<3>WZ	439	4	439	ECL	NXP PAR<7>WZ	539	4	539	ECL	
140	4	140	TTL	CCURD IA FRRWZ	240	4	240	TTL	CCURD IA FRRWZ	340	4	340	TTL	CCURD IA FRRWZ	440	4	440	TTL	CCURD IA FRRWZ	540	4	540	TTL	

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TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0
REVISED: Wed Mar 6 17:50:12 1991

ABBR: IOBP
ENGR: NOBLES

PAGE: 7

NIA P5 CONNECTOR

AUGAT-NGC-800
 BACKPLANE VERSION
 800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
 IA 1P
 BOARD=NIA

TOP
 J5
 J4
 J3
 J2
 J1
 BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.

SEC=5

GROUND ALL PINS ON THIS ROW!

101	5 101	EGL	XC IA SCAN CTL<2>IN21	201	5 201	EGL	XC IA SCAN CTL<1>IN21	301	5 301	EGL	XC IA SCAN CTL<0>IN21	401	5 401	EGL	IA XC HARD ERRORIN20	501	5 501	
102	5 102	EGL	XC IA SCAN INN21	202	5 202	EGL	IA XC SCAN OUTIN20	302	5 302	EGL	IA XSE.WH PAR<3>IN20	402	5 402	EGL	IA XSO.WH PAR<3>IN20	502	5 502	
103	5 103	EGL	IA XSE.WH PAR<2>IN20	203	5 203	EGL	IA XSO.WH PAR<2>IN20	303	5 303	EGL	IA XSE.WH DATA<2>IN20	403	5 403	EGL	IA XSO.WH DATA<2>IN20	503	5 503	
104	5 104	EGL	IA XSE.WH DATA<1>IN20	204	5 204	EGL	IA XSO.WH DATA<1>IN20	304	5 304	EGL	IA XSE.WH DATA<0>IN20	404	5 404	EGL	IA XSO.WH DATA<0>IN20	504	5 504	
105	5 105	EGL	IA XSE.WH DATA<14>IN20	205	5 205	EGL	IA XSO.WH DATA<14>IN20	305	5 305	EGL	IA XSE.WH DATA<13>IN20	405	5 405	EGL	IA XSO.WH DATA<13>IN20	505	5 505	
106	5 106	EGL	IA XSE.WH DATA<13>IN20	206	5 206	EGL	IA XSO.WH DATA<13>IN20	306	5 306	EGL	IA XSE.WH DATA<12>IN20	406	5 406	EGL	IA XSO.WH DATA<12>IN20	506	5 506	
107	5 107	EGL	IA XSE.WH DATA<12>IN20	207	5 207	EGL	IA XSO.WH DATA<12>IN20	307	5 307	EGL	IA XSE.WH DATA<11>IN20	407	5 407	EGL	IA XSO.WH DATA<11>IN20	507	5 507	
108	5 108	EGL	IA XSE.WH DATA<11>IN20	208	5 208	EGL	IA XSO.WH DATA<11>IN20	308	5 308	EGL	IA XSE.WH DATA<10>IN20	408	5 408	EGL	IA XSO.WH DATA<10>IN20	508	5 508	
109	5 109	EGL	IA XSE.WH DATA<10>IN20	209	5 209	EGL	IA XSO.WH DATA<10>IN20	309	5 309	EGL	IA XSE.WH DATA<9>IN20	409	5 409	EGL	IA XSO.WH DATA<9>IN20	509	5 509	
110	5 110	EGL	IA XSE.WH DATA<9>IN20	210	5 210	EGL	IA XSO.WH DATA<9>IN20	310	5 310	EGL	IA XSE.WH DATA<8>IN20	410	5 410	EGL	IA XSO.WH DATA<8>IN20	510	5 510	
111	5 111	EGL	IA XSE.WH DATA<8>IN20	211	5 211	EGL	IA XSO.WH DATA<8>IN20	311	5 311	EGL	IA XSE.ADDR<15>IN20	411	5 411	EGL	IA XSO.ADDR<15>IN20	511	5 511	
112	5 112	EGL	IA XSE.ADDR<15>IN20	212	5 212	EGL	IA XSO.ADDR<15>IN20	312	5 312	EGL	IA XSE.ADDR<14>IN20	412	5 412	EGL	IA XSO.ADDR<14>IN20	512	5 512	
113	5 113	EGL	IA XSE.ADDR<14>IN20	213	5 213	EGL	IA XSO.ADDR<14>IN20	313	5 313	EGL	IA XSE.ADDR<13>IN20	413	5 413	EGL	IA XSO.ADDR<13>IN20	513	5 513	
114	5 114	EGL	IA XSE.ADDR<13>IN20	214	5 214	EGL	IA XSO.ADDR<13>IN20	314	5 314	EGL	IA XSE.ADDR<12>IN20	414	5 414	EGL	IA XSO.ADDR<12>IN20	514	5 514	
115	5 115	EGL	IA XSE.CYCLE<15>IN20	215	5 215	EGL	IA XSO.CYCLE<15>IN20	315	5 315	EGL	IA XSE.ADDR<11>IN20	415	5 415	EGL	IA XSO.ADDR<11>IN20	515	5 515	
116	5 116	EGL	IA XSE.CYCLE<14>IN20	216	5 216	EGL	IA XSO.CYCLE<14>IN20	316	5 316	EGL	IA XSE.ADDR<10>IN20	416	5 416	EGL	IA XSO.ADDR<10>IN20	516	5 516	
117	5 117	EGL	IA XSE.BD SEL<15>IN20	217	5 217	EGL	IA XSO.BD SEL<15>IN20	317	5 317	EGL	IA XSE.ADDR<9>IN20	417	5 417	EGL	IA XSO.ADDR<9>IN20	517	5 517	
118	5 118	EGL	IA XSE.BD SEL<14>IN20	218	5 218	EGL	IA XSO.BD SEL<14>IN20	318	5 318	EGL	IA XSE.ADDR<8>IN20	418	5 418	EGL	IA XSO.ADDR<8>IN20	518	5 518	
119	5 119	EGL	IA XSE.BD SEL<13>IN20	219	5 219	EGL	IA XSO.BD SEL<13>IN20	319	5 319	EGL	IA XSE.ADDR<7>IN20	419	5 419	EGL	IA XSO.ADDR<7>IN20	519	5 519	
120	5 120	EGL	IA XSE.BD SEL<12>IN20	220	5 220	EGL	IA XSO.BD SEL<12>IN20	320	5 320	EGL	IA XSE.ADDR<6>IN20	420	5 420	EGL	IA XSO.ADDR<6>IN20	520	5 520	
121	5 121	EGL	IA XSE.WH PAR<0>IN20	221	5 221	EGL	IA XSO.WH PAR<0>IN20	321	5 321	EGL	IA XSE.WH PAR<1>IN20	421	5 421	EGL	IA XSO.WH PAR<1>IN20	521	5 521	
122	5 122	EGL	IA XSE.WH DATA<0>IN20	222	5 222	EGL	IA XSO.WH DATA<0>IN20	322	5 322	EGL	IA XSE.WH DATA<1>IN20	422	5 422	EGL	IA XSO.WH DATA<1>IN20	522	5 522	
123	5 123	EGL	IA XSE.WH DATA<1>IN20	223	5 223	EGL	IA XSO.WH DATA<1>IN20	323	5 323	EGL	IA XSE.WH DATA<2>IN20	423	5 423	EGL	IA XSO.WH DATA<2>IN20	523	5 523	
124	5 124	EGL	IA XSE.WH DATA<2>IN20	224	5 224	EGL	IA XSO.WH DATA<2>IN20	324	5 324	EGL	IA XSE.WH DATA<3>IN20	424	5 424	EGL	IA XSO.WH DATA<3>IN20	524	5 524	
125	5 125	EGL	IA XSE.WH DATA<28>IN20	225	5 225	EGL	IA XSO.WH DATA<28>IN20	325	5 325	EGL	IA XSE.WH DATA<29>IN20	425	5 425	EGL	IA XSO.WH DATA<29>IN20	525	5 525	
126	5 126	EGL	IA XSE.WH DATA<27>IN20	226	5 226	EGL	IA XSO.WH DATA<27>IN20	326	5 326	EGL	IA XSE.WH DATA<19>IN20	426	5 426	EGL	IA XSO.WH DATA<19>IN20	526	5 526	
127	5 127	EGL	IA XSE.WH DATA<18>IN20	227	5 227	EGL	IA XSO.WH DATA<18>IN20	327	5 327	EGL	IA XSE.WH DATA<19>IN20	427	5 427	EGL	IA XSO.WH DATA<19>IN20	527	5 527	
128	5 128	EGL	IA XSE.WH DATA<17>IN20	228	5 228	EGL	IA XSO.WH DATA<17>IN20	328	5 328	EGL	IA XSE.WH DATA<17>IN20	428	5 428	EGL	IA XSO.WH DATA<17>IN20	528	5 528	
129	5 129	EGL	IA XSE.WH DATA<16>IN20	229	5 229	EGL	IA XSO.WH DATA<16>IN20	329	5 329	EGL	IA XSE.WH DATA<16>IN20	429	5 429	EGL	IA XSO.WH DATA<16>IN20	529	5 529	
130	5 130	EGL	IA XSE.ADDR<17>IN20	230	5 230	EGL	IA XSO.ADDR<17>IN20	330	5 330	EGL	IA XSE.ADDR<28>IN20	430	5 430	EGL	IA XSO.ADDR<28>IN20	530	5 530	
131	5 131	EGL	IA XSE.ADDR<16>IN20	231	5 231	EGL	IA XSO.ADDR<16>IN20	331	5 331	EGL	IA XSE.ADDR<27>IN20	431	5 431	EGL	IA XSO.ADDR<27>IN20	531	5 531	
132	5 132	EGL	IA XSE.CIL PAR<3>IN20	232	5 232	EGL	IA XSO.CIL PAR<3>IN20	332	5 332	EGL	IA XSE.ADDR<26>IN20	432	5 432	EGL	IA XSO.ADDR<26>IN20	532	5 532	
133	5 133	EGL	IA XSE.CIL PAR<2>IN20	233	5 233	EGL	IA XSO.CIL PAR<2>IN20	333	5 333	EGL	IA XSE.ADDR<25>IN20	433	5 433	EGL	IA XSO.ADDR<25>IN20	533	5 533	
134	5 134	EGL	IA XSE.CIL PAR<1>IN20	234	5 234	EGL	IA XSO.CIL PAR<1>IN20	334	5 334	EGL	IA XSE.ADDR<24>IN20	434	5 434	EGL	IA XSO.ADDR<24>IN20	534	5 534	
135	5 135	EGL	IA XSE.CIL PAR<0>IN20	235	5 235	EGL	IA XSO.CIL PAR<0>IN20	335	5 335	EGL	IA XSE.ADDR<23>IN20	435	5 435	EGL	IA XSO.ADDR<23>IN20	535	5 535	
136	5 136	EGL	IA XSE.CIL PAR<0>IN20	236	5 236	EGL	IA XSO.CIL PAR<0>IN20	336	5 336	EGL	IA XSE.ADDR<22>IN20	436	5 436	EGL	IA XSO.ADDR<22>IN20	536	5 536	
137	5 137	EGL	IA XSE.WH ZONE<3>IN20	237	5 237	EGL	IA XSO.WH ZONE<3>IN20	337	5 337	EGL	IA XSE.ADDR<21>IN20	437	5 437	EGL	IA XSO.ADDR<21>IN20	537	5 537	
138	5 138	EGL	IA XSE.WH ZONE<2>IN20	238	5 238	EGL	IA XSO.WH ZONE<2>IN20	338	5 338	EGL	IA XSE.ADDR<20>IN20	438	5 438	EGL	IA XSO.ADDR<20>IN20	538	5 538	
139	5 139	EGL	IA XSE.WH ZONE<1>IN20	239	5 239	EGL	IA XSO.WH ZONE<1>IN20	339	5 339	EGL	IA XSE.ADDR<19>IN20	439	5 439	EGL	IA XSO.ADDR<19>IN20	539	5 539	
140	5 140	EGL	IA XSE.WH ZONE<0>IN20	240	5 240	EGL	IA XSO.WH ZONE<0>IN20	340	5 340	EGL	IA XSE.ADDR<18>IN20	440	5 440	EGL	IA XSO.ADDR<18>IN20	540	5 540	GND VG

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TITLE: IOBP
 DRAWING: 411-001251-300C Rev 0.0
 REVISED: Wed Mar 6 17:51:49 1991

ABBR: IOBP
 ENGR: NOBLES

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7

6

5

4

3

2

1

NCU P1 CONNECTOR

NOTE: NCU IS SLOT 8 IN THE I/O BAY BACKPLANE.

AUGAT-NGC-800
BACKPLANE VERSION
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
CU I P
BOARD=NCU

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.

SEC=1

GROUND ALL PINS ON THIS ROW!

101	1 101	TL	WS CU ADBUS<31>
102	1 102	TL	WS CU ADBUS<30>
103	1 103	TL	WS CU ADBUS<29>
104	1 104	TL	WS CU ADBUS<28>
105	1 105	TL	WS CU ADBUS<27>
106	1 106	TL	WS CU ADBUS<26>
107	1 107	TL	WS CU ADBUS<25>
108	1 108	TL	WS CU ADBUS<24>
109	1 109	TL	WS CU ADBUS<23>
110	1 110	TL	WS CU ADBUS<22>
111	1 111	TL	WS CU ADBUS<21>
112	1 112	TL	WS CU ADBUS<20>
113	1 113	TL	WS CU ADBUS<19>
114	1 114	TL	WS CU ADBUS<18>
115	1 115	TL	WS CU ADBUS<17>
116	1 116	TL	WS CU ADBUS<16>
117	1 117	TL	WS CU ADIFAR<02>
118	1 118	TL	WS CU ADIFAR<12>
119	1 119	TL	WS CUDAS
120	1 120	TL	WS CUDS
121	1 121	TL	WS CU HADDR
122	1 122	TL	WS CU HSEI
123	1 123	TL	WS CU SPARE3
124	1 124	TL	
125	1 125	TL	
126	1 126	TL	
127	1 127	EG	SERIAL NUMBER<15>
128	1 128	EG	SERIAL NUMBER<14>
129	1 129	EG	SERIAL NUMBER<13>
130	1 130	EG	SERIAL NUMBER<12>
131	1 131	EG	SERIAL PU
132	1 132	EG	XIOP0 IA BUS REQ
133	1 133	EG	IA XIOP HD VAL
134	1 134	EG	XIOP IACBUI AVL
135	1 135	EG	IA XIOP BUS GRH
136	1 136	EG	BP CU PORTID<2>
137	1 137	EG	CU BP LBD INI OUT
138	1 138	EG	XIOP0 IA ERR
139	1 139	EG	BP CU SLOTID<3>
140	1 140	EG	BP CU CLOCK 2X

201	1 201	TL	WS CU ADBUS<31>
202	1 202	TL	WS CU ADBUS<30>
203	1 203	TL	WS CU ADBUS<29>
204	1 204	TL	WS CU ADBUS<28>
205	1 205	TL	WS CU ADBUS<27>
206	1 206	TL	WS CU ADBUS<26>
207	1 207	TL	WS CU ADBUS<25>
208	1 208	TL	WS CU ADBUS<24>
209	1 209	TL	WS CU ADBUS<23>
210	1 210	TL	WS CU ADBUS<22>
211	1 211	TL	WS CU ADBUS<21>
212	1 212	TL	WS CU ADBUS<20>
213	1 213	TL	WS CU ADBUS<19>
214	1 214	TL	WS CU ADBUS<18>
215	1 215	TL	WS CU ADBUS<17>
216	1 216	TL	WS CU ADBUS<16>
217	1 217	TL	WS CU ADIFAR<05>
218	1 218	TL	WS CU ADIFAR<12>
219	1 219	TL	WS CUDAS
220	1 220	TL	WS CUDS
221	1 221	TL	WS CU HADDR
222	1 222	TL	WS CU HSEI
223	1 223	TL	WS CU SPARE3
224	1 224	TL	
225	1 225	TL	
226	1 226	TL	
227	1 227	EG	SERIAL NUMBER<11>
228	1 228	EG	SERIAL NUMBER<10>
229	1 229	EG	SERIAL NUMBER<9>
230	1 230	EG	SERIAL NUMBER<8>
231	1 231	EG	
232	1 232	EG	IA XIOP0 BUS GNT
233	1 233	EG	XIOP IA WRTI VAL
234	1 234	EG	IA XIOP MBUF AVL
235	1 235	EG	IA XIOP CLK SYNC
236	1 236	EG	BP CU PORTID<2>
237	1 237	EG	BP CU LBD INI IN
238	1 238	EG	NC
239	1 239	EG	BP CU SLOTID<3>
240	1 240	EG	BP CU CLOCK 2X

301	1 301	TL	WS CU ADBUS<15>
302	1 302	TL	WS CU ADBUS<14>
303	1 303	TL	WS CU ADBUS<13>
304	1 304	TL	WS CU ADBUS<12>
305	1 305	TL	WS CU ADBUS<11>
306	1 306	TL	WS CU ADBUS<10>
307	1 307	TL	WS CU ADBUS<9>
308	1 308	TL	WS CU ADBUS<8>
309	1 309	TL	WS CU ADBUS<7>
310	1 310	TL	WS CU ADBUS<6>
311	1 311	TL	WS CU ADBUS<5>
312	1 312	TL	WS CU ADBUS<4>
313	1 313	TL	WS CU ADBUS<3>
314	1 314	TL	WS CU ADBUS<2>
315	1 315	TL	WS CU ADBUS<1>
316	1 316	TL	WS CU ADBUS<02>
317	1 317	TL	WS CU ADIFAR<22>
318	1 318	TL	WS CU ADIFAR<32>
319	1 319	TL	CU WS ACK
320	1 320	TL	CU WS ERR
321	1 321	TL	CU WS INT
322	1 322	TL	WS CU SPARE2
323	1 323	TL	WS CU SPARE1
324	1 324	TL	
325	1 325	TL	
326	1 326	TL	
327	1 327	EG	SERIAL NUMBER<7>
328	1 328	EG	SERIAL NUMBER<6>
329	1 329	EG	SERIAL NUMBER<5>
330	1 330	EG	SERIAL NUMBER<4>
331	1 331	EG	
332	1 332	EG	XIOP0 IA BRG0
333	1 333	EG	NXP IBINI V<2>
334	1 334	EG	NXP IBINI V<6>
335	1 335	EG	NXP IBINI V<2>
336	1 336	EG	NXP IBINI V<4>
337	1 337	EG	BP CU PORTID<2>
338	1 338	EG	IA XIOP IBINI
339	1 339	EG	BP CU SLOTID<3>
340	1 340	EG	BP CU CLOCK 1XPH

401	1 401	TL	WS CU ADBUS<15>
402	1 402	TL	WS CU ADBUS<14>
403	1 403	TL	WS CU ADBUS<13>
404	1 404	TL	WS CU ADBUS<12>
405	1 405	TL	WS CU ADBUS<11>
406	1 406	TL	WS CU ADBUS<10>
407	1 407	TL	WS CU ADBUS<9>
408	1 408	TL	WS CU ADBUS<8>
409	1 409	TL	WS CU ADBUS<7>
410	1 410	TL	WS CU ADBUS<6>
411	1 411	TL	WS CU ADBUS<5>
412	1 412	TL	WS CU ADBUS<4>
413	1 413	TL	WS CU ADBUS<3>
414	1 414	TL	WS CU ADBUS<2>
415	1 415	TL	WS CU ADBUS<1>
416	1 416	TL	WS CU ADBUS<02>
417	1 417	TL	WS CU ADIFAR<22>
418	1 418	TL	WS CU ADIFAR<32>
419	1 419	TL	CU WS ACK
420	1 420	TL	CU WS ERR
421	1 421	TL	CU WS INT
422	1 422	TL	WS CU SPARE2
423	1 423	TL	WS CU SPARE1
424	1 424	TL	
425	1 425	TL	
426	1 426	TL	
427	1 427	EG	SERIAL NUMBER<3>
428	1 428	EG	SERIAL NUMBER<2>
429	1 429	EG	SERIAL NUMBER<1>
430	1 430	EG	SERIAL NUMBER<0>
431	1 431	EG	
432	1 432	EG	IA XIOP0 IBGRNT
433	1 433	EG	NXP IBINI V<3>
434	1 434	EG	NXP IBINI V<2>
435	1 435	EG	NXP IBINI V<1>
436	1 436	EG	NXP IBINI V<0>
437	1 437	EG	BP CU PORTID<0>
438	1 438	EG	NC
439	1 439	EG	BP CU SLOTID<0>
440	1 440	EG	BP CU CLOCK 1XPH

501	1 501	TL	
502	1 502	TL	
503	1 503	TL	
504	1 504	TL	
505	1 505	TL	
506	1 506	TL	
507	1 507	TL	
508	1 508	TL	
509	1 509	TL	
510	1 510	TL	
511	1 511	TL	
512	1 512	TL	
513	1 513	TL	
514	1 514	TL	
515	1 515	TL	
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524	1 524	TL	
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526	1 526	TL	
527	1 527	TL	
528	1 528	TL	
529	1 529	TL	
530	1 530	TL	
531	1 531	TL	
532	1 532	TL	
533	1 533	TL	
534	1 534	TL	
535	1 535	TL	
536	1 536	TL	
537	1 537	TL	
538	1 538	TL	
539	1 539	TL	
540	1 540	TL	GND VG

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TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0

ABBR: IOBP
ENGR: NOBLES

REVISED: Wed Mar 6 17:52:18 1991

PAGE: 9

NCU P2 CONNECTOR

AUGAT-NGC-800
BACKPLANE VERSION
 800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
 CU 1P
 BOARD=NCU

TOP
 J5
 J4
 J3
 J2
 J1
 BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.

SEC=2

GROUND ALL PINS ON THIS ROW!

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THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO CONVEX COMPUTER CORPORATION (CONVEX). USE OR DISCLOSURE WITHOUT THE WRITTEN PERMISSION OF AN OFFICER OF CONVEX IS EXPRESSLY FORBIDDEN. COPYRIGHT (C) CONVEX 1991



TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0

ABBR: IOBP
ENGR: NOBLES

REVISED: Wed Mar 6 17:52:48 1991

PAGE: 10

NCU P3 CONNECTOR

AUGAT-NGC-800
BACKPLANE VERSION
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
CU
1P
BOARD=NCU

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.

SEC=3

GROUND ALL PINS ON THIS ROW!

101	3 101	ea	CU XC.NMB RBE<15>*	201	3 201	ea	CU XC.NMB RBE<11>*	301	3 301	ea	CU XC.NMB RBE<7>*	401	3 401	ea	CU XC.NMB RBE<3>*	501	3 501
102	3 102	ea	CU XC.NMB RBE<14>*	202	3 202	ea	CU XC.NMB RBE<10>*	302	3 302	ea	CU XC.NMB RBE<6>*	402	3 402	ea	CU XC.NMB RBE<2>*	502	3 502
103	3 103	ea	CU XC.NMB RBE<13>*	203	3 203	ea	CU XC.NMB RBE<9>*	303	3 303	ea	CU XC.NMB RBE<5>*	403	3 403	ea	CU XC.NMB RBE<1>*	503	3 503
104	3 104	ea	CU XC.NMB RBE<12>*	204	3 204	ea	CU XC.NMB RBE<8>*	304	3 304	ea	CU XC.NMB RBE<4>*	404	3 404	ea	CU XC.NMB RBE<0>*	504	3 504
105	3 105	ea	CU XC.XCL ADDR<5>	205	3 205	ea	CU XC.XCL ADDR<3>	305	3 305	ea	CU XC.XCL ADDR<1>	405	3 405	ea	CU XC.XCL ADDR<0>	505	3 505
106	3 106	ea	CU XC.XCL ADDR<4>	206	3 206	ea	CU XC.XCL ADDR<2>	306	3 306	ea	CU XC.XCL ADDR<0>	406	3 406	ea	XC CU.NMB SOFT EHH	506	3 506
107	3 107	ea	CU XC.XCL MUX CIL<5>	207	3 207	ea	CU XC.XCL MUX CIL<3>	307	3 307	ea	CU XC.MASTER S0	407	3 407	ea	XC CU.XCL PAR EHH	507	3 507
108	3 108	ea	CU XC.XCL MUX CIL<4>	208	3 208	ea	CU XC.XCL MUX CIL<2>	308	3 308	ea	CU XC.XCL MUX CIL<1>	408	3 408	ea	CU XC.XCL MUX CIL<0>	508	3 508
109	3 109	ea	CU XC.SCAN WH DAT	209	3 209	ea	XC CU.SCAN HD DAT	309	3 309	ea	XC CU.HARD ERROR	409	3 409	ea	XC CU.NIA SOFT EHH	509	3 509
110	3 110	ea		210	3 210	ea	CU XC.XCL WHITE LO	310	3 310	ea	CU XC.XCL WHITE HI	410	3 410	ea	XC CU.SCALAH HAL	510	3 510
111	3 111			211	3 211			311	3 311			411	3 411			511	3 511
112	3 112	ea	CU MB6 CLOCK 2X*	212	3 212	ea	CU MB6 CLOCK 2X	312	3 312	ea	CU MB7 CLOCK 2X*	412	3 412	ea	CU MB7 CLOCK 2X	512	3 512
113	3 113	ea	CU MB4 CLOCK 2X	213	3 213	ea	CU MB4 CLOCK 2X	313	3 313	ea	CU MB5 CLOCK 2X	413	3 413	ea	CU MB5 CLOCK 2X	513	3 513
114	3 114	ea	CU MB2 CLOCK 2X	214	3 214	ea	CU MB2 CLOCK 2X	314	3 314	ea	CU MB3 CLOCK 2X	414	3 414	ea	CU MB3 CLOCK 2X	514	3 514
115	3 115	ea	CU MB1 CLOCK 2X	215	3 215	ea	CU MB1 CLOCK 2X	315	3 315	ea	CU MB0 CLOCK 2X*	415	3 415	ea	CU MB0 CLOCK 2X	515	3 515
116	3 116	ea	CU BP CLOCK 1XFH	216	3 216	ea	CU BP CLOCK 1XFH	316	3 316	ea	CU BP CLOCK 2X	416	3 416	ea	CU BP CLOCK 2X	516	3 516
117	3 117	ea	CU XC CLOCK 1XFH	217	3 217	ea	CU XC CLOCK 1XFH	317	3 317	ea	CU XST1 CLOCK 1X	417	3 417	ea	CU XST1 CLOCK 1X	517	3 517
118	3 118	ea	CU XC CLOCK 1X	218	3 218	ea	CU XC CLOCK 1X	318	3 318	ea	CU XHE CLOCK 1X	418	3 418	ea	CU XHE CLOCK 1X	518	3 518
119	3 119	ea	CU XSOE CLOCK 1X	219	3 219	ea	CU XSOE CLOCK 1X	319	3 319	ea	CU XSO0 CLOCK 1X	419	3 419	ea	CU XSO0 CLOCK 1X	519	3 519
120	3 120	ea	BAY2S5 CLOCK 1X	220	3 220	ea	BAY2S5 CLOCK 1X	320	3 320	ea	CU XST0 CLOCK 1X	420	3 420	ea	CU XST0 CLOCK 1X	520	3 520
121	3 121	ea	BAY2S4 CLOCK 1X	221	3 221	ea	BAY2S4 CLOCK 1X	321	3 321	ea	CU XHO CLOCK 1X	421	3 421	ea	CU XHO CLOCK 1X	521	3 521
122	3 122	ea	CU IA7 CLOCK 3X	222	3 222	ea	CU IA7 CLOCK 3X	322	3 322	ea	CU IA6 CLOCK 3X	422	3 422	ea	CU IA6 CLOCK 3X	522	3 522
123	3 123	ea	CU IA5 CLOCK 3X	223	3 223	ea	CU IA5 CLOCK 3X	323	3 323	ea	CU IA4 CLOCK 3X	423	3 423	ea	CU IA4 CLOCK 3X	523	3 523
124	3 124	ea	CU IA3 CLOCK 3X	224	3 224	ea	CU IA3 CLOCK 3X	324	3 324	ea	CU IA2 CLOCK 3X	424	3 424	ea	CU IA2 CLOCK 3X	524	3 524
125	3 125	ea	CU IA1 CLOCK 3X	225	3 225	ea	CU IA1 CLOCK 3X	325	3 325	ea	CU IA0 CLOCK 3X	425	3 425	ea	CU IA0 CLOCK 3X	525	3 525
126	3 126	ea	CU SP0 CLOCK 2X	226	3 226	ea	CU SP0 CLOCK 2X	326	3 326	ea	CU IA8 CLOCK 3X	426	3 426	ea	CU IA8 CLOCK 3X	526	3 526
127	3 127	ea	CU VP0 CLOCK 3X	227	3 227	ea	CU VP0 CLOCK 3X	327	3 327	ea	CU SP1 CLOCK 2X	427	3 427	ea	CU SP1 CLOCK 2X	527	3 527
128	3 128	ea	CU SP2 CLOCK 2X	228	3 228	ea	CU SP2 CLOCK 2X	328	3 328	ea	CU VP1 CLOCK 3X	428	3 428	ea	CU VP1 CLOCK 3X	528	3 528
129	3 129	ea	CU VP2 CLOCK 3X	229	3 229	ea	CU VP2 CLOCK 3X	329	3 329	ea	CU SP3 CLOCK 2X	429	3 429	ea	CU SP3 CLOCK 2X	529	3 529
130	3 130	ea	CU IA8 CLOCK 2X	230	3 230	ea	CU IA8 CLOCK 2X	330	3 330	ea	CU VP3 CLOCK 3X	430	3 430	ea	CU VP3 CLOCK 3X	530	3 530
131	3 131	ea	CU SP4 CLOCK 2X	231	3 231	ea	CU SP4 CLOCK 2X	331	3 331	ea	CU BP XIOP CLOCK 2X	431	3 431	ea	CU BP XIOP CLOCK 2X	531	3 531
132	3 132	ea	CU SP5 CLOCK 2X	232	3 232	ea	CU SP5 CLOCK 2X	332	3 332	ea	CU VP4 CLOCK 3X	432	3 432	ea	CU VP4 CLOCK 3X	532	3 532
133	3 133	ea	CU SP6 CLOCK 2X	233	3 233	ea	CU SP6 CLOCK 2X	333	3 333	ea	CU VP5 CLOCK 3X	433	3 433	ea	CU VP5 CLOCK 3X	533	3 533
134	3 134	ea	CU SP7 CLOCK 2X	234	3 234	ea	CU SP7 CLOCK 2X	334	3 334	ea	CU VP6 CLOCK 3X	434	3 434	ea	CU VP6 CLOCK 3X	534	3 534
135	3 135	ea	SPARE CLOCK	235	3 235	ea	SPARE CLOCK	335	3 335	ea	CU VP7 CLOCK 3X	435	3 435	ea	CU VP7 CLOCK 3X	535	3 535
136	3 136	ea	CU XC.XCL DATA IN<1>	236	3 236	ea	XC CU.XCL DATO<1>	336	3 336	ea	CU XC.XCL LOOPBACK	436	3 436	ea	CU XC.NMB REFRESH	536	3 536
137	3 137	ea	CU XC.XCL DATA IN<2>	237	3 237	ea	XC CU.XCL DATO<2>	337	3 337	ea	CU XC.RECHIC	437	3 437	ea	CU XC.XBAR REFRESH	537	3 537
138	3 138	ea	CU XC.XCL DATA IN<3>	238	3 238	ea	XC CU.XCL DATO<3>	338	3 338	ea	CU XC.RECHIC	438	3 438	ea	CU XC.XBAR REFRESH	538	3 538
139	3 139	ea	CU XC.XCL DATA IN<4>	239	3 239	ea	XC CU.XCL DATO<4>	339	3 339	ea	CU XC.XCL DATI PAR<3>	439	3 439	ea	XC CU.XCL DATO PAR<3>	539	3 539
140	3 140	ea	CU XC.XCL DATA IN<5>	240	3 240	ea	XC CU.XCL DATO<5>	340	3 340	ea	CU XC.XCL DATA IN<5>	440	3 440	ea	XC CU.XCL DATO<5>	540	3 540

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TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0

ABBR: IOBP

ENGR: NOBLES

REVISED: Wed Mar 6 17:53:18 1991

PAGE: 11

NCU P5 CONNECTOR

AUGAT-NGC-800
BACKPLANE VERSION
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
CU 1P
BOARD=NCU

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=5

GROUND ALL PINS ON THIS ROW

<table border="1" style="width: 100%; border-collapse: collapse;"> <tr><td>101</td><td>5 101</td><td>ECL</td><td>XC CU SCAN CTL<2></td></tr> <tr><td>102</td><td>5 102</td><td>ECL</td><td>XC CU SCAN IN</td></tr> <tr><td>103</td><td>5 103</td><td>ECL</td><td>CU XS0E SEND PAR ERR</td></tr> <tr><td>104</td><td>5 104</td><td>ECL</td><td>XSO CU RDY</td></tr> <tr><td>105</td><td>5 105</td><td>ECL</td><td>CU XC IRAP VEC1<3></td></tr> <tr><td>106</td><td>5 106</td><td>ECL</td><td>CU XC IRAP VEC1<1></td></tr> <tr><td>107</td><td>5 107</td><td>ECL</td><td>CU XC IRAP VEC1<2></td></tr> <tr><td>108</td><td>5 108</td><td>ECL</td><td>CU XC IRAP VEC1<3></td></tr> <tr><td>109</td><td>5 109</td><td>ECL</td><td>CU XC IRAP VEC1<4></td></tr> <tr><td>110</td><td>5 110</td><td>ECL</td><td>CU XC IRAP VEC1<5></td></tr> <tr><td>111</td><td>5 111</td><td>ECL</td><td>CU XC IRAP VEC1<6></td></tr> <tr><td>112</td><td>5 112</td><td>ECL</td><td>CU XC IRAP VEC1<7></td></tr> <tr><td>113</td><td>5 113</td><td>ECL</td><td>CU XC IRAP VEC1<8></td></tr> <tr><td>114</td><td>5 114</td><td>ECL</td><td>CU XC IRAP VEC1<9></td></tr> <tr><td>115</td><td>5 115</td><td>ECL</td><td>CU XC IRAP VEC1<10></td></tr> <tr><td>116</td><td>5 116</td><td>ECL</td><td>CU XC IRAP VEC1<11></td></tr> <tr><td>117</td><td>5 117</td><td>ECL</td><td>CU XC NIA SLOG ENA<5></td></tr> <tr><td>118</td><td>5 118</td><td>ECL</td><td>CU XC NIA CU CLEAR<5></td></tr> <tr><td>119</td><td>5 119</td><td>ECL</td><td>XSO CUCIL PAR<3></td></tr> <tr><td>120</td><td>5 120</td><td>ECL</td><td>XSO CU ADDR<2></td></tr> <tr><td>121</td><td>5 121</td><td>ECL</td><td>XSO CU ADDR<3></td></tr> <tr><td>122</td><td>5 122</td><td>ECL</td><td>XSO CU ADDR<4></td></tr> <tr><td>123</td><td>5 123</td><td>ECL</td><td>XSO CU ADDR<5></td></tr> <tr><td>124</td><td>5 124</td><td>ECL</td><td>XSO CU ADDR<6></td></tr> <tr><td>125</td><td>5 125</td><td>ECL</td><td>XSO CU ADDR<7></td></tr> <tr><td>126</td><td>5 126</td><td>ECL</td><td>XSO CU ADDR<8></td></tr> <tr><td>127</td><td>5 127</td><td>ECL</td><td>XSO CU WH PAR<3></td></tr> <tr><td>128</td><td>5 128</td><td>ECL</td><td>XSO CU WH DATA<2></td></tr> <tr><td>129</td><td>5 129</td><td>ECL</td><td>XSO CU WH DATA<3></td></tr> <tr><td>130</td><td>5 130</td><td>ECL</td><td>XSO CU WH DATA<4></td></tr> <tr><td>131</td><td>5 131</td><td>ECL</td><td>XSO CU WH DATA<5></td></tr> <tr><td>132</td><td>5 132</td><td>ECL</td><td>XSO CU WH DATA<6></td></tr> <tr><td>133</td><td>5 133</td><td>ECL</td><td>XSO CU WH DATA<7></td></tr> <tr><td>134</td><td>5 134</td><td>ECL</td><td>XSO CU WH DATA<8></td></tr> <tr><td>135</td><td>5 135</td><td>ECL</td><td>XSO CU WH DATA<9></td></tr> <tr><td>136</td><td>5 136</td><td>ECL</td><td>CU XRO HD PAR<2></td></tr> <tr><td>137</td><td>5 137</td><td>ECL</td><td>CU XRO HD DATA<2></td></tr> <tr><td>138</td><td>5 138</td><td>ECL</td><td>CU XRO HD DATA<3></td></tr> <tr><td>139</td><td>5 139</td><td>ECL</td><td>CU XRO HD DATA<4></td></tr> <tr><td>140</td><td>5 140</td><td>ECL</td><td>CU XRO HD DATA<5></td></tr> </table>	101	5 101	ECL	XC CU SCAN CTL<2>	102	5 102	ECL	XC CU SCAN IN	103	5 103	ECL	CU XS0E SEND PAR ERR	104	5 104	ECL	XSO CU RDY	105	5 105	ECL	CU XC IRAP VEC1<3>	106	5 106	ECL	CU XC IRAP VEC1<1>	107	5 107	ECL	CU XC IRAP VEC1<2>	108	5 108	ECL	CU XC IRAP VEC1<3>	109	5 109	ECL	CU XC IRAP VEC1<4>	110	5 110	ECL	CU XC IRAP VEC1<5>	111	5 111	ECL	CU XC IRAP VEC1<6>	112	5 112	ECL	CU XC IRAP VEC1<7>	113	5 113	ECL	CU XC IRAP VEC1<8>	114	5 114	ECL	CU XC IRAP VEC1<9>	115	5 115	ECL	CU XC IRAP VEC1<10>	116	5 116	ECL	CU XC IRAP VEC1<11>	117	5 117	ECL	CU XC NIA SLOG ENA<5>	118	5 118	ECL	CU XC NIA CU CLEAR<5>	119	5 119	ECL	XSO CUCIL PAR<3>	120	5 120	ECL	XSO CU ADDR<2>	121	5 121	ECL	XSO CU ADDR<3>	122	5 122	ECL	XSO CU ADDR<4>	123	5 123	ECL	XSO CU ADDR<5>	124	5 124	ECL	XSO CU ADDR<6>	125	5 125	ECL	XSO CU ADDR<7>	126	5 126	ECL	XSO CU ADDR<8>	127	5 127	ECL	XSO CU WH PAR<3>	128	5 128	ECL	XSO CU WH DATA<2>	129	5 129	ECL	XSO CU WH DATA<3>	130	5 130	ECL	XSO CU WH DATA<4>	131	5 131	ECL	XSO CU WH DATA<5>	132	5 132	ECL	XSO CU WH DATA<6>	133	5 133	ECL	XSO CU WH DATA<7>	134	5 134	ECL	XSO CU WH DATA<8>	135	5 135	ECL	XSO CU WH DATA<9>	136	5 136	ECL	CU XRO HD PAR<2>	137	5 137	ECL	CU XRO HD DATA<2>	138	5 138	ECL	CU XRO HD DATA<3>	139	5 139	ECL	CU XRO HD DATA<4>	140	5 140	ECL	CU XRO HD DATA<5>	<table border="1" style="width: 100%; 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THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO CONVEX COMPUTER CORPORATION (CONVEX). USE OR DISCLOSURE WITHOUT THE WRITTEN PERMISSION OF AN OFFICER OF CONVEX IS EXPRESSLY FORBIDDEN. COPYRIGHT (C) CONVEX 1991



TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0
REVISED: Wed Mar 6 17:54:18 1991

ABBR: IOBP
ENGR: NOBLES

PAGE: 13

XIOP P1 CONNECTOR

NOTE: XIOP IS SLOT 9 IN THE I/O BAY BACKPLANE.

AUGAT-NGC-800
BACKPLANE VERSION
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
XP 1P
BOARD=XIOP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.

SEC=1

GROUND ALL PINS ON THIS ROW!

101	1 101	ECL	P20<1>
102	1 102	ECL	P20<3>
103	1 103	ECL	P20<5>
104	1 104	ECL	P20<7>
105	1 105	ECL	P20<9>
106	1 106	ECL	P20<11>
107	1 107	ECL	P20<13>
108	1 108	ECL	P20<15>
109	1 109	ECL	P20<17>
110	1 110	ECL	P20<19>
111	1 111	ECL	P20<21>
112	1 112	ECL	P20<23>
113	1 113	ECL	P20<25>
114	1 114	ECL	P20<27>
115	1 115	ECL	P20<29>
116	1 116	ECL	P20<31>
117	1 117	ECL	P20<33>
118	1 118	ECL	P20<35>
119	1 119	ECL	P20<37>
120	1 120	ECL	P20<39>
121	1 121	ECL	P20<41>
122	1 122	ECL	P20<43>
123	1 123	ECL	P20<45>
124	1 124	ECL	P20<47>
125	1 125	ECL	P20<49>
126	1 126	ECL	P20<51>
127	1 127	ECL	P20<53>
128	1 128	ECL	P20<55>
129	1 129	ECL	P20<57>
130	1 130	ECL	P20<59>
131	1 131	ECL	P20<61>
132	1 132	ECL	P20<63>
133	1 133	ECL	P20<65>
134	1 134	ECL	P20<67>
135	1 135	ECL	P20<69>
136	1 136	ECL	BP XIOP.PORTRID<3>
137	1 137	ECL	BP XIOP.LBD INTL OUT
138	1 138	ECL	XIOP1A.ERH
139	1 139	ECL	BP XIOP.SLOTID<3>
140	1 140	ECL	CU XIOP.CLOCK 2X

201	1 201	ECL	P20<2>
202	1 202	ECL	P20<4>
203	1 203	ECL	P20<6>
204	1 204	ECL	P20<8>
205	1 205	ECL	P20<10>
206	1 206	ECL	P20<12>
207	1 207	ECL	P20<14>
208	1 208	ECL	P20<16>
209	1 209	ECL	P20<18>
210	1 210	ECL	P20<20>
211	1 211	ECL	P20<22>
212	1 212	ECL	P20<24>
213	1 213	ECL	P20<26>
214	1 214	ECL	P20<28>
215	1 215	ECL	P20<30>
216	1 216	ECL	P20<32>
217	1 217	ECL	P20<34>
218	1 218	ECL	P20<36>
219	1 219	ECL	P20<38>
220	1 220	ECL	P20<40>
221	1 221	ECL	P20<42>
222	1 222	ECL	P20<44>
223	1 223	ECL	P20<46>
224	1 224	ECL	P20<48>
225	1 225	ECL	P20<50>
226	1 226	ECL	P20<52>
227	1 227	ECL	P20<54>
228	1 228	ECL	P20<56>
229	1 229	ECL	P20<58>
230	1 230	ECL	P20<60>
231	1 231	ECL	P20<62>
232	1 232	ECL	P20<64>
233	1 233	ECL	P20<66>
234	1 234	ECL	P20<68>
235	1 235	ECL	P20<70>
236	1 236	ECL	BP XIOP.PORTRID<2>
237	1 237	ECL	BP XIOP.LBD INTL IN
238	1 238	ECL	NC
239	1 239	ECL	BP XIOP.SLOTID<2>
240	1 240	ECL	CU XIOP.CLOCK 2X

301	1 301	ECL	P20<77>
302	1 302	ECL	P20<79>
303	1 303	NC	
304	1 304	NC	
305	1 305	NC	
306	1 306	NC	
307	1 307	NC	
308	1 308	NC	
309	1 309	NC	
310	1 310	NC	
311	1 311	NC	
312	1 312	NC	
313	1 313	NC	
314	1 314	NC	
315	1 315	NC	
316	1 316	NC	
317	1 317	NC	
318	1 318	NC	
319	1 319	NC	
320	1 320	NC	
321	1 321	NC	
322	1 322	NC	
323	1 323	NC	
324	1 324	NC	
325	1 325	NC	
326	1 326	NC	
327	1 327	NC	
328	1 328	NC	
329	1 329	NC	
330	1 330	NC	
331	1 331	NC	
332	1 332	ECL	XIOP1A.BUS.HEO
333	1 333	ECL	IA XIOP.HD.VAL
334	1 334	ECL	XIOP1A.CBUF.AVL
335	1 335	ECL	IA XIOP.BUS.ERH
336	1 336	ECL	XIOP1A.IBREQ
337	1 337	ECL	NXP.IBINTV<7>
338	1 338	ECL	NXP.IBINTV<6>
339	1 339	ECL	NXP.IBINTV<5>
340	1 340	ECL	NXP.IBINTV<4>
341	1 341	ECL	BP XIOP.PORTRID<1>
342	1 342	ECL	IA XIOP.IBINT
343	1 343	ECL	BP XIOP.SLOTID<1>
344	1 344	ECL	NC

401	1 401	ECL	P20<78>
402	1 402	ECL	P20<80>
403	1 403	NC	
404	1 404	NC	
405	1 405	NC	
406	1 406	NC	
407	1 407	NC	
408	1 408	NC	
409	1 409	NC	
410	1 410	NC	
411	1 411	NC	
412	1 412	NC	
413	1 413	NC	
414	1 414	NC	
415	1 415	NC	
416	1 416	NC	
417	1 417	NC	
418	1 418	NC	
419	1 419	NC	
420	1 420	NC	
421	1 421	NC	
422	1 422	NC	
423	1 423	NC	
424	1 424	NC	
425	1 425	NC	
426	1 426	NC	
427	1 427	NC	
428	1 428	NC	
429	1 429	NC	
430	1 430	NC	
431	1 431	ECL	IA XIOP1.BUS.GNI
432	1 432	ECL	XIOP1A.WH1.VAL
433	1 433	ECL	IA XIOP.MBUF.AVL
434	1 434	ECL	IA XIOP.CLK.SYNC
435	1 435	ECL	IA XIOP1.IBSRINT
436	1 436	ECL	NXP.IBINTV<3>
437	1 437	ECL	NXP.IBINTV<2>
438	1 438	ECL	NXP.IBINTV<1>
439	1 439	ECL	NXP.IBINTV<0>
440	1 440	ECL	BP XIOP.PORTRID<0>
441	1 441	ECL	NC
442	1 442	ECL	BP XIOP.SLOTID<0>
443	1 443	ECL	NC
444	1 444	ECL	NC

501	1 501		
502	1 502		
503	1 503		
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TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0
REVISED: Wed Mar 6 17:54:48 1991

ABBR: IOBP
ENGR: NOBELS

PAGE: 14

XIOP P2 CONNECTOR

AUGAT-NGC-800
BACKPLANE VERSION
 800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
 XP 1P
 BOARD=XIOP

TOP
 J5
 J4
 J3
 J2
 J1
 BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.

SEC=2

GROUND ALL PINS ON THIS ROW!

101	2 101	ECL	P21<97>
102	2 102	ECL	P21<99>
103	2 100	ECL	P21<1>
104	2 104	ECL	P21<3>
105	2 105	ECL	P21<5>
106	2 106	ECL	P21<7>
107	2 107	ECL	P21<9>
108	2 108	ECL	P21<11>
109	2 109	ECL	P21<13>
110	2 110	ECL	P21<15>
111	2 111	ECL	P21<17>
112	2 112	ECL	P21<19>
113	2 113	ECL	P21<21>
114	2 114	ECL	P21<23>
115	2 115	ECL	P21<25>
116	2 116	ECL	P21<27>
117	2 117	ECL	P21<29>
118	2 118	ECL	P21<31>
119	2 119	ECL	P21<33>
120	2 120	ECL	P21<35>
121	2 121	ECL	P21<37>
122	2 122	ECL	P21<39>
123	2 123	ECL	P21<41>
124	2 124	ECL	P21<43>
125	2 125	ECL	P21<45>
126	2 126	ECL	P21<47>
127	2 127	ECL	P21<49>
128	2 128	ECL	P20<51>
129	2 129	ECL	P20<53>
130	2 130	ECL	P20<55>
131	2 131	ECL	P20<57>
132	2 132	ECL	P20<59>
133	2 133	ECL	P20<61>
134	2 134	ECL	P20<63>
135	2 135	ECL	P20<65>
136	2 136	ECL	P20<67>
137	2 137	ECL	P20<69>
138	2 138	ECL	P20<71>
139	2 139	ECL	P20<73>
140	2 140	ECL	P20<75>

201	2 201	ECL	P21<98>
202	2 202	ECL	P21<100>
203	2 200	ECL	P21<2>
204	2 204	ECL	P21<4>
205	2 205	ECL	P21<6>
206	2 206	ECL	P21<8>
207	2 207	ECL	P21<10>
208	2 208	ECL	P21<12>
209	2 209	ECL	P21<14>
210	2 210	ECL	P21<16>
211	2 211	ECL	P21<18>
212	2 212	ECL	P21<20>
213	2 213	ECL	P21<22>
214	2 214	ECL	P21<24>
215	2 215	ECL	P21<26>
216	2 216	ECL	P21<28>
217	2 217	ECL	P21<30>
218	2 218	ECL	P21<32>
219	2 219	ECL	P21<34>
220	2 220	ECL	P21<36>
221	2 221	ECL	P21<38>
222	2 222	ECL	P21<40>
223	2 223	ECL	P21<42>
224	2 224	ECL	P21<44>
225	2 225	ECL	P21<46>
226	2 226	ECL	P21<48>
227	2 227	ECL	P21<50>
228	2 228	ECL	P20<52>
229	2 229	ECL	P20<54>
230	2 230	ECL	P20<56>
231	2 231	ECL	P20<58>
232	2 232	ECL	P20<60>
233	2 233	ECL	P20<62>
234	2 234	ECL	P20<64>
235	2 235	ECL	P20<66>
236	2 236	ECL	P20<68>
237	2 237	ECL	P20<70>
238	2 238	ECL	P20<72>
239	2 239	ECL	P20<74>
240	2 240	ECL	P20<76>

301	2 301	ECL	P21<93>
302	2 302	ECL	P21<95>
303	2 300		NC
304	2 304		NC
305	2 305		NC
306	2 306		NC
307	2 307		NC
308	2 308		NC
309	2 309		NC
310	2 310		NC
311	2 311		NC
312	2 312		NC
313	2 313		NC
314	2 314		NC
315	2 315		NC
316	2 316		NC
317	2 317		NC
318	2 318		NC
319	2 319		NC
320	2 320		NC
321	2 321		NC
322	2 322		NC
323	2 323	ECL	NXP DATA<36>
324	2 324	ECL	NXP DATA<37>
325	2 325	ECL	NXP DATA<38>
326	2 326	ECL	NXP DATA<39>
327	2 327	ECL	NXP DATA<40>
328	2 328	ECL	NXP DATA<41>
329	2 329	ECL	NXP DATA<42>
330	2 330	ECL	NXP DATA<43>
331	2 331	ECL	NXP PAH<4>
332	2 332	ECL	NXP DATA<48>
333	2 333	ECL	NXP DATA<49>
334	2 334	ECL	NXP DATA<50>
335	2 335	ECL	NXP DATA<51>
336	2 336	ECL	NXP DATA<52>
337	2 337	ECL	NXP DATA<53>
338	2 338	ECL	NXP DATA<54>
339	2 339	ECL	NXP DATA<55>
340	2 340	ECL	NXP PAH<5>

401	2 401	ECL	P21<94>
402	2 402	ECL	P21<96>
403	2 403		NC
404	2 404		NC
405	2 405		NC
406	2 406		NC
407	2 407		NC
408	2 408		NC
409	2 409		NC
410	2 410		NC
411	2 411		NC
412	2 412		NC
413	2 413		NC
414	2 414		NC
415	2 415		NC
416	2 416		NC
417	2 417		NC
418	2 418		NC
419	2 419		NC
420	2 420		NC
421	2 421		NC
422	2 422		NC
423	2 423	ECL	NXP DATA<44>
424	2 424	ECL	NXP DATA<45>
425	2 425	ECL	NXP DATA<46>
426	2 426	ECL	NXP DATA<47>
427	2 427	ECL	NXP DATA<48>
428	2 428	ECL	NXP DATA<49>
429	2 429	ECL	NXP DATA<50>
430	2 430	ECL	NXP DATA<51>
431	2 431	ECL	NXP PAH<4>
432	2 432	ECL	NXP DATA<18>
433	2 433	ECL	NXP DATA<17>
434	2 434	ECL	NXP DATA<18>
435	2 435	ECL	NXP DATA<19>
436	2 436	ECL	NXP DATA<20>
437	2 437	ECL	NXP DATA<21>
438	2 438	ECL	NXP DATA<22>
439	2 439	ECL	NXP DATA<23>
440	2 440	ECL	NXP PAH<5>

501	2 501		
502	2 502		
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540	2 540	GND VG	

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TITLE: IOBP
 DRAWING: 411-001251-300C Rev 0.0
 REVISED: Wed Mar 6 17:55:16 1991

ABBR: IOBP
 ENGR: NOBLES

PAGE: 15

XIOP P4 CONNECTOR

AUGAT-NGC-800
BACKPLANE VERSION
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
XP 1P
BOARD=XIOP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.

SEC=4

GROUND ALL PINS ON THIS ROW!

101	4 101	ECL	P23<77>
102	4 102	ECL	P23<79>
103	4 103	ECL	P23<81>
104	4 104	ECL	P23<83>
105	4 105	ECL	P23<85>
106	4 106	ECL	P23<87>
107	4 107	ECL	P23<89>
108	4 108	ECL	P23<91>
109	4 109	ECL	P23<93>
110	4 110	ECL	P23<95>
111	4 111	ECL	P23<97>
112	4 112	ECL	P23<99>
113	4 113	ECL	P22<51>
114	4 114	ECL	P22<53>
115	4 115	ECL	P22<55>
116	4 116	ECL	P22<57>
117	4 117	ECL	P22<59>
118	4 118	ECL	P22<61>
119	4 119	ECL	P22<63>
120	4 120	ECL	P22<65>
121	4 121	ECL	P22<67>
122	4 122	ECL	P22<69>
123	4 123	ECL	P22<71>
124	4 124	ECL	P22<73>
125	4 125	ECL	P22<75>
126	4 126	ECL	P22<77>
127	4 127	ECL	P22<79>
128	4 128	ECL	P22<81>
129	4 129	ECL	P22<83>
130	4 130	ECL	P22<85>
131	4 131	ECL	P22<87>
132	4 132	ECL	P22<89>
133	4 133	ECL	P22<91>
134	4 134	ECL	P22<93>
135	4 135	ECL	P22<95>
136	4 136	ECL	P22<97>
137	4 137	ECL	P22<99>
138	4 138	ECL	P22<1>
139	4 139	ECL	P22<3>
140	4 140	ECL	P22<5>

201	4 201	ECL	P23<78>
202	4 202	ECL	P23<80>
203	4 203	ECL	P23<82>
204	4 204	ECL	P23<84>
205	4 205	ECL	P23<86>
206	4 206	ECL	P23<88>
207	4 207	ECL	P23<90>
208	4 208	ECL	P23<92>
209	4 209	ECL	P23<94>
210	4 210	ECL	P23<96>
211	4 211	ECL	P23<98>
212	4 212	ECL	P23<100>
213	4 213	ECL	P22<62>
214	4 214	ECL	P22<64>
215	4 215	ECL	P22<66>
216	4 216	ECL	P22<68>
217	4 217	ECL	P22<70>
218	4 218	ECL	P22<72>
219	4 219	ECL	P22<74>
220	4 220	ECL	P22<76>
221	4 221	ECL	P22<78>
222	4 222	ECL	P22<80>
223	4 223	ECL	P22<82>
224	4 224	ECL	P22<84>
225	4 225	ECL	P22<86>
226	4 226	ECL	P22<88>
227	4 227	ECL	P22<90>
228	4 228	ECL	P22<92>
229	4 229	ECL	P22<94>
230	4 230	ECL	P22<96>
231	4 231	ECL	P22<98>
232	4 232	ECL	P22<99>
233	4 233	ECL	P22<97>
234	4 234	ECL	P22<95>
235	4 235	ECL	P22<93>
236	4 236	ECL	P22<91>
237	4 237	ECL	P22<89>
238	4 238	ECL	P22<87>
239	4 239	ECL	P22<85>
240	4 240	ECL	P22<83>

301	4 301		NC
302	4 302		NC
303	4 303		NC
304	4 304		NC
305	4 305		NC
306	4 306		NC
307	4 307		NC
308	4 308		NC
309	4 309		NC
310	4 310		NC
311	4 311		NC
312	4 312		NC
313	4 313		NC
314	4 314		NC
315	4 315		NC
316	4 316		NC
317	4 317		NC
318	4 318		NC
319	4 319		NC
320	4 320		NC
321	4 321		NC
322	4 322		NC
323	4 323	ECL	NXP DATA<40>
324	4 324	ECL	NXP DATA<41>
325	4 325	ECL	NXP DATA<42>
326	4 326	ECL	NXP DATA<43>
327	4 327	ECL	NXP DATA<44>
328	4 328	ECL	NXP DATA<45>
329	4 329	ECL	NXP DATA<46>
330	4 330	ECL	NXP DATA<47>
331	4 331	ECL	NXP PAH<25>
332	4 332	ECL	NXP DATA<32>
333	4 333	ECL	NXP DATA<33>
334	4 334	ECL	NXP DATA<34>
335	4 335	ECL	NXP DATA<35>
336	4 336	ECL	NXP DATA<36>
337	4 337	ECL	NXP DATA<37>
338	4 338	ECL	NXP DATA<38>
339	4 339	ECL	NXP DATA<39>
340	4 340	ECL	NXP PAH<33>

401	4 401		NC
402	4 402		NC
403	4 403		NC
404	4 404		NC
405	4 405		NC
406	4 406		NC
407	4 407		NC
408	4 408		NC
409	4 409		NC
410	4 410		NC
411	4 411		NC
412	4 412		NC
413	4 413		NC
414	4 414		NC
415	4 415		NC
416	4 416		NC
417	4 417		NC
418	4 418		NC
419	4 419		NC
420	4 420		NC
421	4 421		NC
422	4 422		NC
423	4 423	ECL	NXP DATA<8>
424	4 424	ECL	NXP DATA<9>
425	4 425	ECL	NXP DATA<10>
426	4 426	ECL	NXP DATA<11>
427	4 427	ECL	NXP DATA<12>
428	4 428	ECL	NXP DATA<13>
429	4 429	ECL	NXP DATA<14>
430	4 430	ECL	NXP DATA<15>
431	4 431	ECL	NXP PAH<6>
432	4 432	ECL	NXP DATA<30>
433	4 433	ECL	NXP DATA<31>
434	4 434	ECL	NXP DATA<32>
435	4 435	ECL	NXP DATA<33>
436	4 436	ECL	NXP DATA<34>
437	4 437	ECL	NXP DATA<35>
438	4 438	ECL	NXP DATA<36>
439	4 439	ECL	NXP DATA<37>
440	4 440	ECL	NXP PAH<7>

501	4 501		
502	4 502		
503	4 503		
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TITLE: IOBP
DRAWING: 411-001251-300C Rev 0.0
REVISED: Wed Mar 6 17:56:09 1991

ABBR: IOBP
ENGR: NOBLES

PAGE: 17

XIOP P5 CONNECTOR

AUGAT-NGC-800
 BACKPLANE VERSION
 800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
 XP 1P
 BOARD=XIOP

TOP
 J5
 J4
 J3
 J2
 J1
 BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED SECTION COMMAND WILL NOT WORK.
 SEC=5

GROUND ALL PINS ON THIS ROW

101	5 101	EGL	XC HSC_SCAN CTL<2>
102	5 102	EGL	XC HSC_SCAN IN
103	5 103	EGL	P23<1>
104	5 104	EGL	P23<2>
105	5 105	EGL	P23<3>
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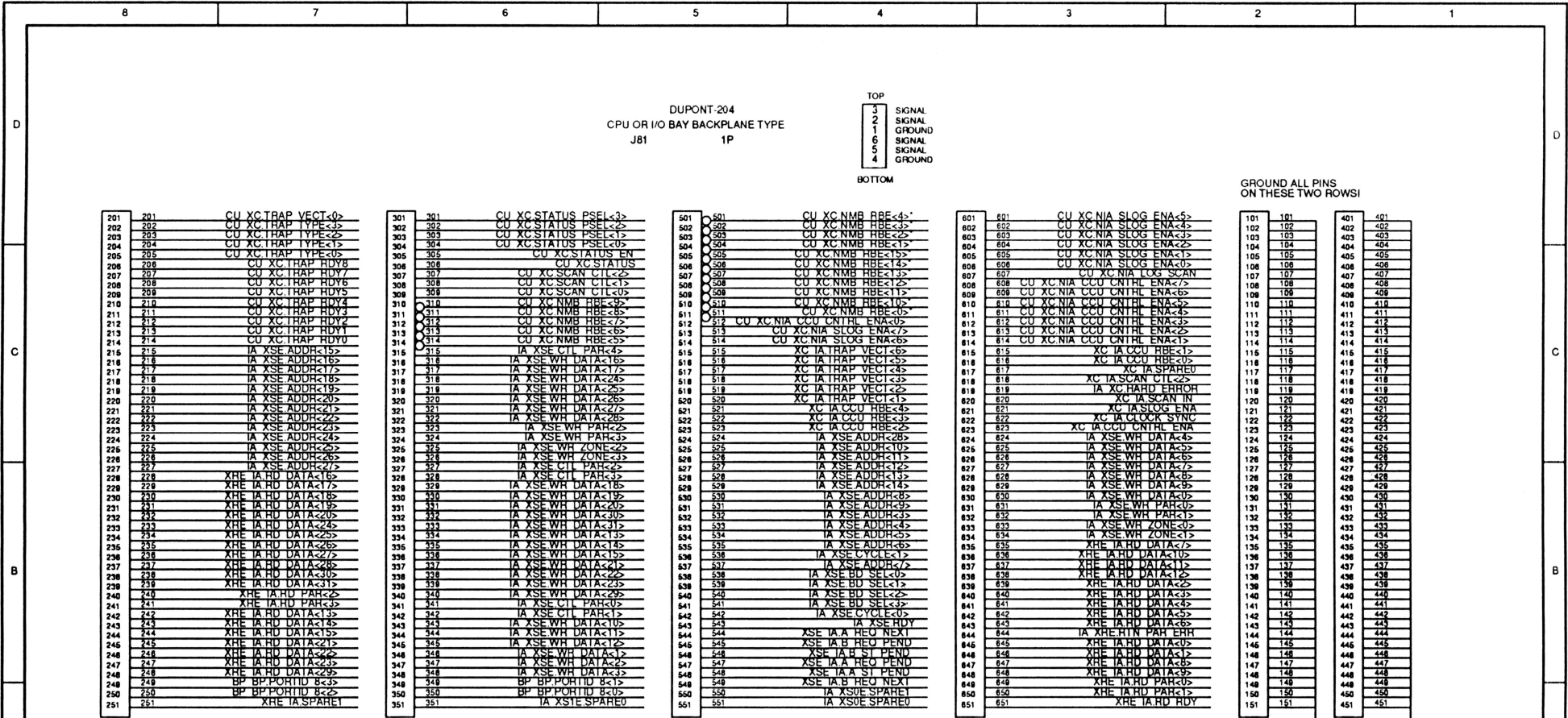
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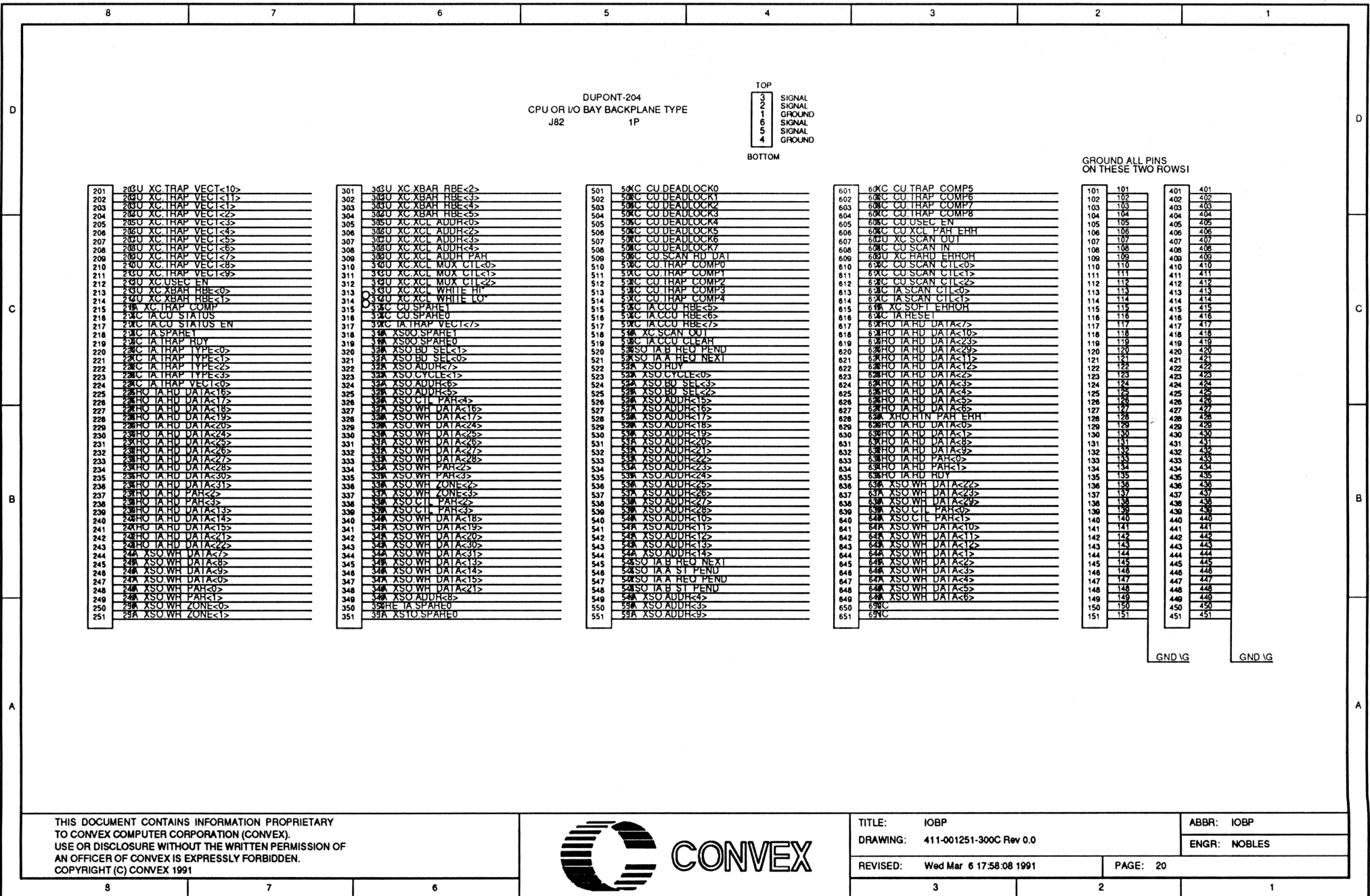
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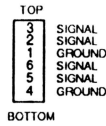
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8 7 6 5 4 3 2 1

DUPONT-204
CPU OR I/O BAY BACKPLANE TYPE
J83 1P



GROUND ALL PINS
ON THESE TWO ROWS!

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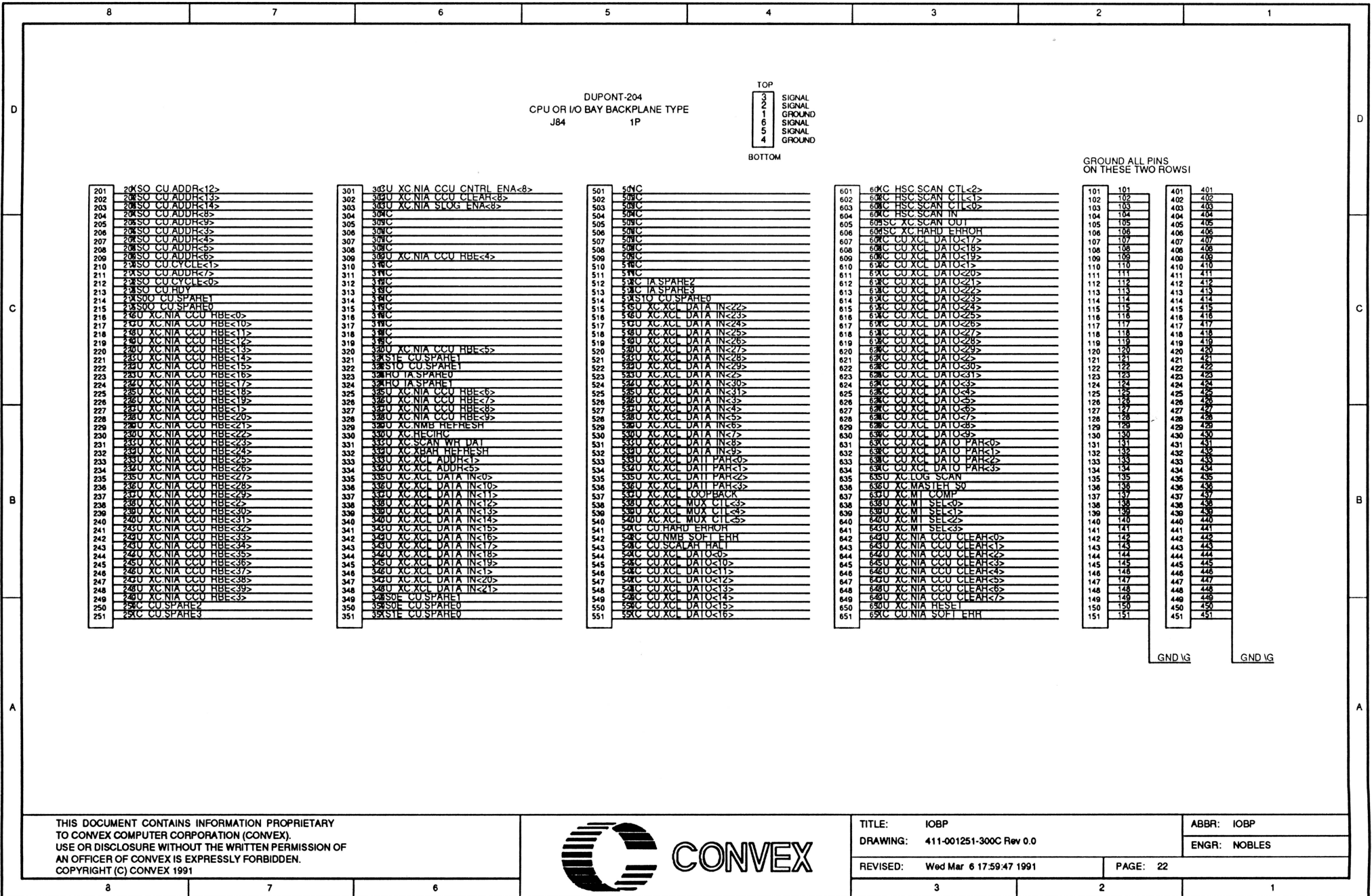
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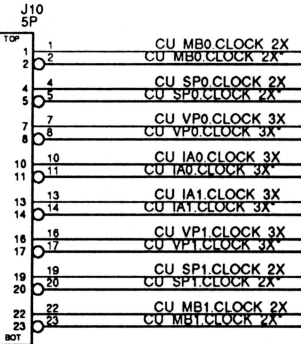
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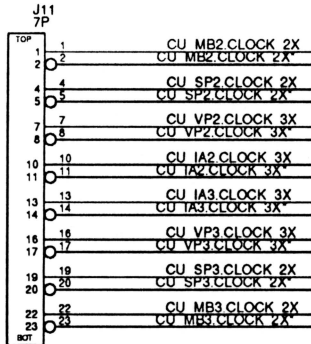
PORTS 0 & 1

C3 CLOCK CONNECTOR
24 PIN PRESSFIT, 3X8 ROW



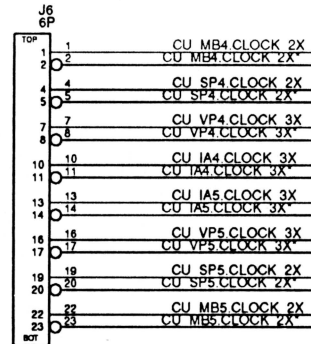
PORTS 2 & 3

C3 CLOCK CONNECTOR
24 PIN PRESSFIT, 3X8 ROW



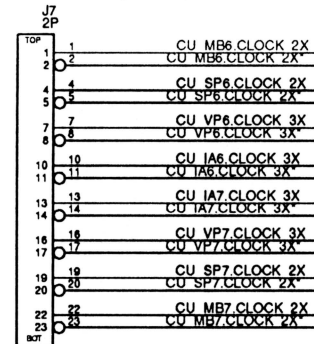
PORTS 4 & 5

C3 CLOCK CONNECTOR
24 PIN PRESSFIT, 3X8 ROW



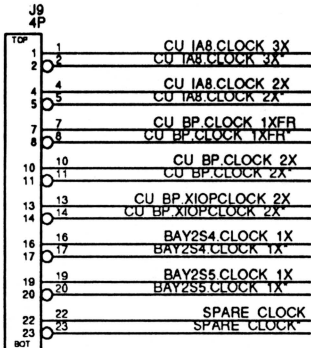
PORTS 6 & 7

C3 CLOCK CONNECTOR
24 PIN PRESSFIT, 3X8 ROW



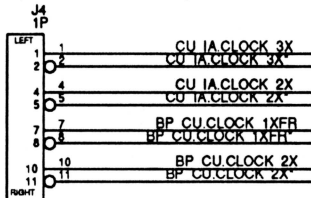
PORT 8 (I/O BAY)

C3 CLOCK CONNECTOR
24 PIN PRESSFIT, 3X8 ROW



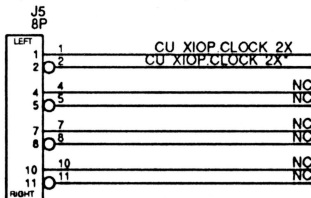
NIA AND NCU

C3 CLOCK CONNECTOR
12 PIN STRAIGHT, 3X4 ROW
(& 8A VIEWED FROM INSIDE CARDCAGE)



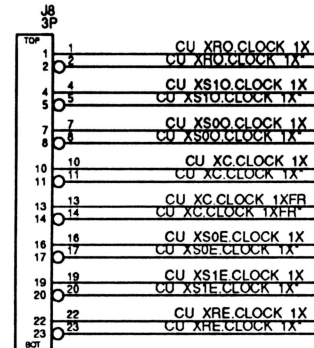
XIOP

C3 CLOCK CONNECTOR
12 PIN STRAIGHT, 3X4 ROW
(& 8A VIEWED FROM INSIDE CARDCAGE)



XBAR

C3 CLOCK CONNECTOR
24 PIN PRESSFIT, 3X8 ROW



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NCU - WORKSTATION INTERFACE

AMPLIMITE100 301-000033-001
 FEMALE, STRAIGHT, SOLDER
 J24
 1P

1	1	WS INTLK
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27	27	WS CU ADBUS<20>
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31	31	WS CU ADBUS<18>
33	33	WS CU ADBUS<17>
35	35	WS CU ADBUS<16>
37	37	WS CU ADPAH<0>
39	39	WS CU ADPAH<1>
41	41	WS CU AS
43	43	WS CU DS
45	45	WS CU RADDR
47	47	WS CU RESET
49	49	WS CU SPARE3

2	2	
4	4	
6	6	WS CU ADBUS<31>
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50	50	WS CU SPARE3

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95	95	WS CU SPARE1
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99	99	

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90	90	CU WS EHR
92	92	CU WS INT1
94	94	WS CU SPARE2
96	96	WS CU SPARE1
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AMPLIMITE100 301-000033-001
 FEMALE, STRAIGHT, SOLDER
 J21
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AMPLIMITE 100 301-000033-001
FEMALE, STRAIGHT, SOLDER
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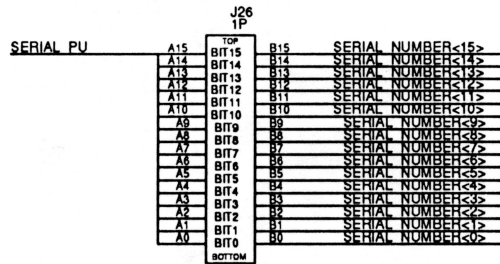
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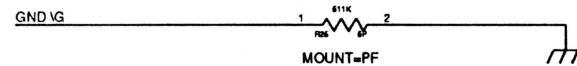
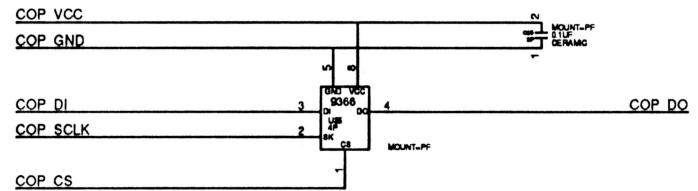
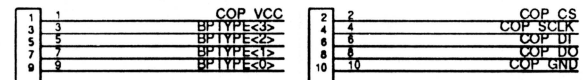
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COP AND SERIAL NUMBER

C3 SERIAL NUMBER CONNECTOR
32 PIN STRAIGHT, 2X16 ROW
(VIEWED FROM INBIDE CARDSAGE)



HDR10
301-000035-004
MALE PRESSFIT-STRAIGHT-4 WALL-LONG EJECTOR
J26 1P



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SYNONYMED INTERCONNECT

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BP IA.SLOTID<3> 5P GND VG
 BP IA.SLOTID<2> 4P NC
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 BP IA.SLOTID<0> 2P NC

IA BP.RBD INTL OUT 1P BP IA.RBD INTL IN

BPTYPE<3> 31P NC
 BPTYPE<2> 30P COP GND
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